

Manuel Hermenegildo

Areas of Interest

Advanced Program Development Technology, Global Program Analysis / Optimization / Verification, Abstract Interpretation, Abstract Machines, Parallelizing Compilers, Resource-Aware Distributed Execution and Logical Agents, Automatic Documentation Tools, Constraint/Logic/Functional Programming Theory and Implementation, Execution Visualization, Sequential and Parallel Computer Architecture.

Education

- Aug 1986 **PhD.**, Electrical and Computer Eng., University of Texas at Austin.
Aug 1984 **M. S.**, Electrical and Computer Eng., University of Texas at Austin.
Jun 1981 **M. S.**, Electrical Eng., Technical U. of Madrid.

Positions

- Jan 2007-... **Scientific Director**, *Madrid Institute for Advanced Research in Software Development Technology (IMDEA-Software, www.software.imdea.org); Madrid, Spain, and*
- Jan 1990-... **Full Professor** (January 1990-May 1994 **Associate Professor**), *Technical U. of Madrid, Department of Computer Science; Spain.*
General scientific coordination of the Institute. Research. Graduate and undergraduate teaching. PhD. student supervision. Leader of the CLIP (Computational Logic, Implementation and Parallelism) Lab. Principal investigator in several EU, international, and national projects on automatic parallelization, verification, abstraction carrying code, global analysis of programs, constraint logic programming, implementation of concurrent constraint languages, program debugging, automatic Web-site generation, next generation program development environments, and others.
- Feb 03-Dec 08 **Prince of Asturias Endowed Chair** in Information Science and Technology, *U. of New Mexico, Departments of Computer Science and Electrical and Computer Engineering; Albuquerque, NM, USA*
Research. Graduate and undergraduate teaching. PhD. student supervision. International student supervision under exchange programs.
- May 00-Sep 02 **Director** of the *Spanish National Research Directorate* (Spanish central agency financing research in all areas of science and technology).
Creation of the directorate from a number of previously existing units. Design and management of all programs funding projects, human resources, and infrastructures, including starting the "Ramon y Cajal" fellowship program. Coordination with regional, European, and international programs. Benchmarking of the national S&T system. Member, board of directors of the Spanish Foundation for Science and Technology, the Spanish Scientific Research Council (CSIC), and the Spanish Center for Industrial and Technological Development (CDTI). Spanish representative in the European Union Advisory Committee for Research (CREST).
- Oct 86-Dec 89 **Member, Technical Staff**, *MCC, Parallel Processing Program and ACA/Systems Technology Lab.; Austin, Texas.*
Principal Investigator, PAL project: parallel and sequential high performance implementation of next generation languages and systems. Development of theoretical bases, execution models, and actual implementations of high-performance parallel logic programming systems. Design and simulation of coherent-cache, cluster-based multiprocessors. Performance studies of execution models on several parallel architectures and organizations.

- Mar 87-Jan 90 **Assistant/Associate Professor** (adjunct), *University of Texas at Austin*, Computer Science Department.
Teaching (Computer Architecture, Logic Programming). Supervision of PhD. students working in the areas of parallel computer architecture, parallel execution model theory and design, abstract interpretation, and automatic parallelization.
- Jun 85-Aug 85 **Intern**, *MCC*; Austin, Texas.
- Jul 84-Aug 84 **Intern**, *IBM Corp.*; Austin, Texas.
- Nov 82-Aug 83 **Analyst**, *Philips S.A.*; Madrid, Spain.
Technical management.
- Jun 81-Aug 81 **Design Engineer** (praktikum), *Siemens GmbH.*; Munich, Germany.
Electronic circuit design and embedded systems development for audio and automotive applications.
- Jan 80-Oct 82 **President**, *HC Electronics*; Madrid, Spain.
Co-owned venture, involved in the development and licensing of electronic circuit designs and prototypes. Products included a Speech Scrambler marketed by Eurotronica S.A., and a Spectrum Scanning System installed at the National Optics Institute.

Other Selected Activities, Honors, and Awards

- “Julio Rey Pastor” **Prize for Research in Mathematics and Information and Communication Technologies**, 2006 (highest Spanish national scientific award in the areas of mathematics and information technology). “Aritmel” **Prize for Scientific Merit in Computer Science**, 2005. Elected member of the **Academia Europaea**.
- **Invited talks and/or tutorials** at RuleML’11, WLPE’11, TAPAS’10 (at SAS 2010), Datalog2.0 (2010), ProTest 09 workshop, 30 Years of Abstract Interpretation POPL’08 meeting, European Computer Science Summit’08, DAMP’07, EEAI’06, DAMP’06, ACM SAC’06, PDP’05, EUROPAR’04, EU-IST GRIDS’04, SAS’03, ICALP’02, ERCIM/CoLogNet’02, LPAR’01, CL’00, CM-PP’00, AgentLink’99 WS, ICLP’99, AFLP’98, EUROPAR’97, ILPS’97, ICLP’97, PAAM / PAP’97, CCL’96, LPAR’94, JICSLP’92, SAS’92, IEEE CompEuro 92, ICLP’89 and at several Workshops and Summer Schools.
- **Associate Editor** of the “*Journal of New Generation Computing*” (Springer-Verlag), **Editorial Adviser** and former **Area Editor** (architecture and implementation) of “*Theory and Practice of Logic Programming*” (Cambridge U. Press) (formerly, the “*Journal of Logic Programming*”, Elsevier North-Holland), **Area Editor** of “*Journal of Applied Logic*” (Elsevier North-Holland), former **Associate Editor** of “*ACM Transactions on Programming Languages and Systems*” (ACM).
- **Program Committee Chairman or Co-Chairman**: “2010 International Conference on Logic Programming”, 2010, Edinburgh, UK, “Verification, Abstract Interpretation and Model Checking” VMCAI 2010, Madrid, Spain, “2008 Declarative Aspects of Multicore Programming” (San Francisco, CA, USA), “2008 International Symposium on Functional and Logic Programming (FLOPS’08)” (Ise, Japan), “2005 Practical Aspects of Declarative Programming Symposium” PADL’05 (Long Beach, CA, USA), “2002 Static Analysis Symposium”, SAS’02 (Madrid, Spain), “Parallel Symbolic Computing” track of EUROPAR’97, 1997 (Passau, Germany); “Programming Languages: Implementations, Logics, and Programs”, 1994 (Madrid, Spain) and 1995 (Utrecht, The Netherlands); “North American Conference on Logic Programming”, 1990, Austin, TX.
- **Conference Chair or Co-Chair**, “ACM Principles of Programming Languages” POPL 2010, Madrid, Spain, “International Conference on Logic Programming” ICLP 2006, Seattle, WA, USA, “2005 Symposium on Practical Aspects of Declarative Languages” PADL’05, Long Beach, CA, USA, “2002 Static Analysis Symposium” SAS’02, Madrid, Spain, Joint “Algebraic and Logic

Programming” and *Programming Languages: Implementations, Logics, and Programs*” conferences, 1994, Madrid, Spain, *First Spanish Conference on Declarative Programming*, PROLE’92, as well as a number of workshops since 1987.

- **Program Committee Member** for several conferences including PLDI’11, DAMP’11, ICLP’10, VMCAI’10, ICLP’08, SAS’08, ServiceWave’08, DAMP’08, FLOPS’08, TGC’07, SAS’07, TGC’06, FLOPS’06, PADL’06, ESOP’05, ICALP’05, PPSWR’05, AEPIA’05, PROLE’05, PADL’05, ICLP’04, EUMAS’04, PDCS’04, PROLE’04, PPDP’03, CP’03, PADL’03, EUMAS’03, PPDP’02, SAS’02, ICLP’01, SAS01, LOPSTR’01, POPL’00, LOPSTR’00, CMPP’00, ESOP’00, ECAI’00 CLPWS, PPDP’99, FLOPS’99, PADL’99, Irregular’99, POPL’97, ICLP’97, DPLE’97, CP’95, Europar’95, PLILP’94-95, PARLE’93, GULP-PRODE’92-94, PLILP’93-95, JICSLP’92, PAP’91, NACLP’90, ICLP’90, ICLP’89. **Session chairman** and **panelist** at several conferences and workshops.
- **Member** of ISTAG, the High-level Advisory Group of the Information Society Technologies Research Program (IST) of the European Union for Framework Program VI (2002-2006). President of several working groups. Co-authored several “white books” and policy reports (not included in publications list).
- **Panel Member** ACM 50th Anniversary Workshop on Future Directions in Computer Science, the ALP Strategic Directions Meeting, and at several EU Vth, VIth, and VIIth Framework Program strategic planning meetings (“New Directions for ICTs in FP7 meeting: Grand challenges for basic research,” “Next Generation GRIDs,” “Global Computing,” “Future Software Systems,” “Advanced Embedded Systems,” etc.).
- **Invited speaker** at the Danish EU presidency high-level meeting on the need for a European Research Council.
- **Acted as scientific expert**, at Spanish Senate hearings, on software patents and open source software. Expert and National Representative for the OECD on Dissemination of Scientific Data and Results.
- **Elected President** of the *Association for Logic Programming* (ALP) and the *Spanish Research Consortium in Informatics and Mathematics* (SpaRCIM, Spain’s member in ERCIM).
- **Elected member of the steering boards** of the *European Association for Programming Languages and Systems* (EAPLS), and (twice) of the ALP. Member of the Steering Committee of *ACM Principles of Programming Languages Conference* (POPL) (and POPL’2011 Most Influential Paper Award committee), *Verification, Abstract Interpretation and Model Checking Conference* (VMCAI), *Static Analysis Symposium* (SAS), *Functional and Logic Programming Symposium* (FLOPS), *ACM Principles and Practice of Declarative Programming Symposium* (PPDP), *Federated Logic Conference* (FLoC), and *Declarative Aspects of Multicore Programming Workshop* series.
- **Member of the Scientific Advisory Board** of the Spanish *National Research Council*, *ACM SIGLOG*, *IFCoLog*, and the *Initiative de Recherche et Innovation sur le Logiciel Libre* Center.
- **Best paper award**, at ICLP 2005 (with José Morales, Manuel Carro, and Germán Puebla).
- **Member of the winning team** at the 18th Prolog Programming contest, at ICLP 2011 in Lexington KY (with Michael Leuschel and Antonio Porto).
- Served on **tenure and promotion committees** or as a **department evaluator** for the K.U.Leuven (Belgium), University of Copenhagen (DIKU), T.U. Denmark (TUD), Cambridge U. (U.K.), Cyprus U., INRIA (France), Ecole Normale Supérieure (France), U. of Porto (Portugal), U. of Melbourne (Australia), New Mexico State U. (USA), Ecole Polytechnique (France), Negev U. (Israel), etc. and several Spanish universities. **President** of the Spanish national **research performance evaluation commission** for the areas of Computer Science and Electronics (CNEAI). Member of Department Evaluation Advisory Board at *Informatics Europe*.
- **Area Coordinator** (parallelism and implementation technologies) of the EU Networks of Excellence in Computational Logic COMPULOG and CoLogNet.

- **Invited Lecturer**, October 1996, in the distinguished visitor lecture series of New Mexico State University. Former **lecturer** on the “*IEEE Computer Society Distinguished Visitor Program*”.
- **Invited Scientist**, August-September 1988, at the Japanese Institute for fifth generation computing (ICOT) in Tokyo.
- **Developed a number of practical systems**, which are in the public domain, some of them with thousands of users, including the Ciao programming system, CiaoPP, LPdoc, the PiLLoW library, CP analyzers, PLAI, automatic parallelizers, &-Prolog, VisAndOr, etc.
- Participated in more than 50 **research projects**, in most of them as principal investigator, funded by the European Union and the Spanish Ministries, as well as NSF, MITI, etc. Also in several projects funded by industry.
- **Referee** for several journals (TOPLAS, Computing Reviews, TCS, TPLP/JLP, JSC, JPDC, FGCS, TODS, LOPLAS, IJPP, IEEE-TSE, JFGC, COMPLANG, JFLP, etc.), and conferences (POPL, SAS, IJCAI, ICLP, SLP, ILPS, PPDP, NAACP, JICSLP, ICPP, DCS, CONPAR, CP, ESOP, EUMAS, PAP, PARLE, PLILP, ALP, POPL, WSA, EUROPAR, GULP, ICALP, ICPA, PODS, ASIAN, CAEPIA, PACT, PASCO, PPSWR, etc.).
- **Evaluator** and panel member for several institutions: EU (ESPRIT, IST FP IV, V, VI), NSF, DARPA, DoD, ICOT, ISF, FULBRIGHT, the Australian, Belgian, Danish, Swedish, Spanish, and UK funding agencies, etc.
- **Faculty Adviser** for the ACM student chapter of the T.U. of Madrid.
- **Fulbright-Hays Scholarship** for Graduate Study in the U.S. (1983-84). **U. of Texas Graduate Research Grant** (1984-85). **MCC Graduate Research Grant** (1985-86).
- **Professional Societies**: ACM, IEEE (Computer Society), ALP (Assoc. for Logic Programming), EAPLS, ATI (Professional Computer Engineers' Assoc. of Spain), ERCIM/SpaRCIM, Informatics Europe, among others.
- **Member** of the Tau Beta Pi Engineering Honor Society.

Students and Recent Collaborators

Graduated Ph.D. Students (+ graduating university/current location):

Kalyan Muthukumar (U.T. Austin/IBM San Jose / Apple research), Yow-Yan Lin (UT Austin/Bellcore), María García de la Banda (T.U. Madrid/Melbourne University), Francisco Bueno (T.U. Madrid), Germán Puebla (T.U. Madrid), Pedro López García (T.U. Madrid), Manuel Carro (T.U. Madrid), Daniel Cabeza Gras (T.U. Madrid), Jorge Navas (U. of New Mexico/U. of Singapore), Amadeo Casas (U. of New Mexico/Microsoft), Mario Méndez (U. of New Mexico/U.T. Austin), José Francisco Morales (T.U. Madrid/Complutense U.).

Most recent collaborators (outside the IMDEA SW Institute and UPM):

P. Stuckey (Melbourne U., Australia), K. Marriott (Monash U., Australia), E. Pontelli (New Mexico S.U., USA), G. Gupta (New Mexico S.U., USA), S. Debray (U. of Arizona, USA), U. Montanari (U. Pisa, Italy), F. Rossi (U. Pisa, Italy), K. Shen (Imperial College, UK), M. Codish (Ben Gurion U., Israel), M. Bruynooghe (K.U. Leuven, Belgium), P. Deransart (INRIA, France), J. Maluszynski (Linköping U., Sweden), V. Santos-Costa (U. of Porto, Portugal), J. Gallagher (U. of Roskilde, Denmark), D. Kapur (U. of New Mexico, USA), D. Stefanovic (U. of New Mexico, USA).

Personal data / contact

Name: Manuel V. Hermenegildo.

Birth date: March 13, 1959

Address: IMDEA Software Institute and
Facultad de Informática / Universidad Politécnica de Madrid
28660 Boadilla del Monte, Madrid / SPAIN

Email: herme at fi.upm.es
manuel.hermenegildo at imdea.org

URLs: <http://www.clip.dia.fi.upm.es/~herme>
<http://software.imdea.org/people/manuel.hermenegildo>

Phone: +34-91-336-3734 (IMDEA) / +34-91-336-7435 (UPM)

Fax: +34-91-352-4819

Publications (in reverse chronological order, and classified by venue category)

Publications are classified according to four ranking databases:

- the JCR listings (using for each publication venue the average position in the list in 1998-2008)
- the CORE listings, and
- the CiteSeer impact listings (see also the upgraded CiteSeerX listing).

Each of these databases (except CORE) maps venues to a number between 0 and 1 (or 0 and 100%) which corresponds to the position of the corresponding venue divided by the total number of ranked venues (the lower the position the better). CORE classifies venues, instead, into four discrete ranking categories: A+ (or A*), A, B and C. In order to have a numerical figure with which to compare to the other databases and be able to compute an average value, we have mapped CORE conference rankings A to 33.0%, B to 64.0% and C to 100.0%, and CORE journal rankings A* to top 5%, A to 20%, B to 64% and C to 100%. We obtain an overall numerical ranking for each publication as the **average** of all *available* rankings for the corresponding venue (some venues do not appear in all ranking databases). Finally, publications are classified according to this average. Publications with average ranking 0-33% are considered as **first level**, 33-66% are considered as **second level**, and the rest are considered as **third level**. In the listings, for each publication we report the individual rankings available for the corresponding venue, as well as the global average position, in the form of a percentage.

Articles in First-Level Refereed Conferences and Journals

1. M. V. Hermenegildo, F. Bueno, M. Carro, P. López, E. Mera, J.F. Morales, G. Puebla. *An Overview of Ciao and its Design Philosophy*. Theory and Practice of Logic Programming, To Appear, Cambridge University Press, 2012. <http://arxiv.org/abs/1102.5497>.
JCR: position (ave) top 36%, impact (ave) 1.20, subject(s): COMPUTER SCIENCE, SOFTWARE ENGINEERING – COMPUTER SCIENCE, THEORY & METHODS. CORE: A. Citeseer: position 494/1221 (top 40%), impact 0.66. Average position: top 32%.
2. G. Puebla, E. Albert, M. Hermenegildo. *Efficient Local Unfolding with Ancestor Stacks*. Theory and Practice of Logic Programming, Vol. 11, Num. 1, pages 1–32, Cambridge U. Press, January 2011.
JCR: position (ave) top 36%, impact (ave) 1.20, subject(s): COMPUTER SCIENCE, SOFTWARE ENGINEERING – COMPUTER SCIENCE, THEORY & METHODS. CORE: A. Citeseer: position 494/1221 (top 40%), impact 0.66. Average position: top 32%.
3. E. Albert, P. Arenas, G. Puebla, M. Hermenegildo. *Reduced Certificates in Abstraction-Carrying Code*. Theory and Practice of Logic Programming, To Appear, 2011.

JCR: position (ave) top 36%, impact (ave) 1.20, subject(s): COMPUTER SCIENCE, SOFTWARE ENGINEERING – COMPUTER SCIENCE, THEORY & METHODS. CORE: A. Citeseer: position 494/1221 (top 40%), impact 0.66. Average position: top 32%.

4. D. Cabeza, M. Hermenegildo. *Non-Strict Independence-Based Program Parallelization Using Sharing and Freeness Information*. Theoretical Computer Science, Vol. 46, Num. 410, pages 4704–4723, Elsevier Science, October 2009.
JCR: position (ave) top 59%, impact (ave) 0.60, subject(s): COMPUTER SCIENCE, THEORY & METHODS. CORE: A. Citeseer: position 338/1221 (top 28%), impact 0.91. Average position: top 36%.
5. E. Mera, P. López-García, M. Hermenegildo. *Integrating Software Testing and Run-Time Checking in an Assertion Verification Framework*. 25th International Conference on Logic Programming (ICLP'09), LNCS, Num. 5649, pages 281–295, Springer-Verlag, July 2009.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.
6. P. Chico de Guzmán, M. Carro, M. V. Hermenegildo. *A Tabling Implementation Based on Variables with Multiple Bindings*. International Conference on Logic Programming (ICLP 2009), LNCS, Num. 5649, 15 pages, Springer-Verlag, July 2009.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.
7. M. Marron, D. Kapur, M. Hermenegildo. *Identification of Logically Related Heap Regions*. ISMM'09: Proceedings of the 8th international symposium on Memory management, ACM Press, June 2009.
CORE: A. Citeseer: position 99/1221 (top 8%), impact 1.55. Average position: top 21%.
8. E. Trias, J. Navas, E. S. Ackley, S. Forrest, M. Hermenegildo. *Negative Ternary Set-Sharing*. International Conference on Logic Programming, ICLP, LNCS, Num. 5366, pages 301–316, Springer-Verlag, December 2008.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.
9. P. Chico de Guzmán, M. Carro, M. V. Hermenegildo. *A Sketch of a Complete Scheme for Tabled Execution Based on Program Transformation*. Int'l. Conference on Logic Programming, LNCS, Vol. 5366, pages 795–800, Springer Verlag, December 2008. Short paper.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.
10. A. Casas, M. Carro, M. Hermenegildo. *A High-Level Implementation of Non-Deterministic, Unrestricted, Independent And-Parallelism*. 24th International Conference on Logic Programming (ICLP'08), LNCS, Vol. 5366, pages 651–666, Springer-Verlag, December 2008.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.
11. M. Marron, M. Hermenegildo, D. Kapur, D. Stefanovic. *Efficient Context-Sensitive Shape Analysis with Graph-Based Heap Models*. International Conference on Compiler Construction (CC 2008), Lecture Notes in Computer Science, 15 pages, Springer, April 2008.
CORE: A. CiteseerX position (ave) top 26%, impact (ave) 0.271. Average position: top 30%.
12. P. Pietrzak, M. Hermenegildo. *Automatic Binding-related Error Diagnosis in Logic Programs*. International Conference on Logic Programming (ICLP'07), LNCS, Num. 4670, pages 333–347, Springer-Verlag, September 2007.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.
13. J. Navas, E. Mera, P. López-García, M. Hermenegildo. *User-Definable Resource Bounds Analysis for Logic Programs*. 23rd International Conference on Logic Programming (ICLP'07), Lecture Notes in Computer Science, Vol. 4670, Springer, 2007.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.

14. M. Marron, D. Kapur, D. Stefanovic, M. Hermenegildo. *A Static Heap Analysis for Shape and Connectivity*. Languages and Compilers for Parallel Computing (LCPC'06), Lecture Notes in Computer Science, Vol. 4382, pages 345–363, Springer, November 2006.
Citeseer: position 186/1221 (top 15%), impact 1.22. Average position: top 15%.
15. M. Carro, J. Morales, H.L. Muller, G. Puebla, M. Hermenegildo. *High-Level Languages for Small Devices: A Case Study*. Compilers, Architecture, and Synthesis for Embedded Systems, pages 271–281, ACM Press / Sheridan, October 2006.
CORE: A. CiteseerX position (ave) top 23%, impact (ave) 0.192. Average position: top 28%.
16. E. Mera, P. López-García, G. Puebla, M. Carro, M. Hermenegildo. *Using Combined Static Analysis and Profiling for Logic Program Execution Time Estimation*. 22nd International Conference on Logic Programming (ICLP'06), LNCS, Num. 4079, pages 431–432, Springer-Verlag, August 2006.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.
17. G. Puebla, E. Albert, M. Hermenegildo. *Abstract Interpretation with Specialized Definitions*. The 13th International Static Analysis Symposium (SAS'06), LNCS, Num. 4134, pages 107–126, Springer, August 2006.
CORE: A. Citeseer: position 145/1221 (top 12%), impact 1.35. Average position: top 22%.
18. E. Albert, P. Arenas, G. Puebla, M. Hermenegildo. *Reduced Certificates for Abstraction-Carrying Code*. 22nd International Conference on Logic Programming (ICLP 2006), LNCS, Num. 4079, pages 163–178, Springer-Verlag, August 2006.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.
19. J. Morales, M. Carro, G. Puebla, M. Hermenegildo. *A Generator of Efficient Abstract Machine Implementations and its Application to Emulator Minimization*. International Conference on Logic Programming, LNCS, Num. 3668, pages 21–36, Springer Verlag, October 2005.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.
20. G. Puebla, E. Albert, M. Hermenegildo. *A Generic Framework for the Analysis and Specialization of Logic Programs*. International Conference on Logic Programming (ICLP 2005), LNCS, Num. 3668, pages 407–409, Springer, October 2005. Extended Abstract.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.
21. M. Hermenegildo, G. Puebla, F. Bueno, P. López-García. *Integrated Program Debugging, Verification, and Optimization Using Abstract Interpretation (and The Ciao System Preprocessor)*. Science of Computer Programming, Vol. 58, Num. 1–2, pages 115–140, Elsevier Science, 2005.
JCR: position (ave) top 42%, impact (ave) 0.80, subject(s): COMPUTER SCIENCE, SOFTWARE ENGINEERING – COMPUTER SCIENCE, SOFTWARE, GRAPHICS, PROGRAMMING.
CORE: A. Citeseer: position 185/1221 (top 15%), impact 1.22. Average position: top 26%.
22. E. Albert, G. Puebla, M. Hermenegildo. *Abstract Interpretation-based Mobile Code Certification*. Proc. of International Conference on Logic Programming (ICLP'04), LNCS, Num. 3132, pages 446–447, Springer-Verlag, September 2004. Extended Abstract.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.
23. J. Correas, J. M. Gomez, M. Carro, D. Cabeza, M. Hermenegildo. *A Generic Persistence Model for CLP Systems*. 2003 International Conference on Logic Programming, LNCS, Num. 2916, pages 481–482, Springer-Verlag, December 2003. Extended abstract.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.

24. S. Muñoz, J.J. Moreno-Navarro, M. Hermenegildo. *Efficient Negation Using Abstract Interpretation*. Proc. of the Eighth International Conference on Logic Programming and Automated Reasoning, LNAI, 15 pages, Springer-Verlag, December 2001.
CORE: A. CiteseerX position (ave) top 38%, impact (ave) 0.169. Average position: top 36%.
25. G. Gupta, E. Pontelli, K. Ali, M. Carlsson, M. Hermenegildo. *Parallel Execution of Prolog Programs: a Survey*. ACM Transactions on Programming Languages and Systems, Vol. 23, Num. 4, pages 472–602, ACM Press, July 2001.
JCR: position (ave) top 22%, impact (ave) 1.27, subject(s): COMPUTER SCIENCE, SOFTWARE ENGINEERING – COMPUTER SCIENCE, SOFTWARE, GRAPHICS, PROGRAMMING.
CORE: A*. Citeseer: position 41/1221 (top 3%), impact 1.92. Average position: top 10%.
26. D. Cabeza, M. Hermenegildo. *Distributed WWW Programming using (Ciao) Prolog and the PiLLoW Library*. Theory and Practice of Logic Programming, Vol. 1, Num. 3, pages 251–282, Cambridge U. Press, May 2001.
JCR: position (ave) top 36%, impact (ave) 1.20, subject(s): COMPUTER SCIENCE, SOFTWARE ENGINEERING – COMPUTER SCIENCE, THEORY & METHODS. CORE: A. Citeseer: position 494/1221 (top 40%), impact 0.66. Average position: top 32%.
27. M. Hermenegildo. *Parallelizing Irregular and Pointer-Based Computations Automatically: Perspectives from Logic and Constraint Programming*. Parallel Computing, Vol. 26, Num. 13–14, pages 1685–1708, Elsevier Science, December 2000.
JCR: position (ave) top 46%, impact (ave) 0.75, subject(s): COMPUTER SCIENCE, THEORY & METHODS. CORE: A. Citeseer: position 519/1221 (top 43%), impact 0.63. Average position: top 36%.
28. D. Cabeza, M. Hermenegildo. *A New Module System for Prolog*. International Conference on Computational Logic, CL2000, LNAI, Num. 1861, pages 131–148, Springer-Verlag, July 2000.
CiteseerX position (ave) top 28%, impact (ave) 0.205. Average position: top 28%.
29. M. Hermenegildo. *A Documentation Generator for (C)LP Systems*. International Conference on Computational Logic, CL2000, LNAI, Num. 1861, pages 1345–1361, Springer-Verlag, July 2000.
CiteseerX position (ave) top 28%, impact (ave) 0.205. Average position: top 28%.
30. M. García de la Banda, M. Hermenegildo, K. Marriott. *Independence in CLP Languages*. ACM Transactions on Programming Languages and Systems, Vol. 22, Num. 2, pages 269–339, ACM Press, March 2000.
JCR: position (ave) top 22%, impact (ave) 1.27, subject(s): COMPUTER SCIENCE, SOFTWARE ENGINEERING – COMPUTER SCIENCE, SOFTWARE, GRAPHICS, PROGRAMMING.
CORE: A*. Citeseer: position 41/1221 (top 3%), impact 1.92. Average position: top 10%.
31. M. Hermenegildo, G. Puebla, K. Marriott, P. Stuckey. *Incremental Analysis of Constraint Logic Programs*. ACM Transactions on Programming Languages and Systems, Vol. 22, Num. 2, pages 187–223, ACM Press, March 2000.
JCR: position (ave) top 22%, impact (ave) 1.27, subject(s): COMPUTER SCIENCE, SOFTWARE ENGINEERING – COMPUTER SCIENCE, SOFTWARE, GRAPHICS, PROGRAMMING.
CORE: A*. Citeseer: position 41/1221 (top 3%), impact 1.92. Average position: top 10%.
32. M. Carro, M. Hermenegildo. *Concurrency in Prolog Using Threads and a Shared Database*. 1999 International Conference on Logic Programming, pages 320–334, MIT Press, Cambridge, MA, USA, November 1999.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.

33. G. Puebla, M. Hermenegildo. *Abstract Multiple Specialization and its Application to Program Parallelization*. J. of Logic Programming. Special Issue on Synthesis, Transformation and Analysis of Logic Programs, Vol. 41, Num. 2&3, pages 279–316, Elsevier - North Holland, November 1999.
JCR: position (ave) top 36%, impact (ave) 1.20, subject(s): COMPUTER SCIENCE, SOFTWARE ENGINEERING – COMPUTER SCIENCE, THEORY & METHODS. CORE: A. Citeseer: position 56/1221 (top 5%), impact 1.78. Average position: top 20%.
34. F. Bueno, M. García de la Banda, M. Hermenegildo. *Effectiveness of Abstract Interpretation in Automatic Parallelization: A Case Study in Logic Programming*. ACM Transactions on Programming Languages and Systems, Vol. 21, Num. 2, pages 189–238, ACM Press, March 1999.
JCR: position (ave) top 22%, impact (ave) 1.27, subject(s): COMPUTER SCIENCE, SOFTWARE ENGINEERING – COMPUTER SCIENCE, SOFTWARE, GRAPHICS, PROGRAMMING. CORE: A*. Citeseer: position 41/1221 (top 3%), impact 1.92. Average position: top 10%.
35. K. Muthukumar, F. Bueno, M. García de la Banda, M. Hermenegildo. *Automatic Compile-time Parallelization of Logic Programs for Restricted, Goal-level, Independent And-parallelism*. Journal of Logic Programming, Vol. 38, Num. 2, pages 165–218, Elsevier - North-Holland, February 1999.
JCR: position (ave) top 36%, impact (ave) 1.20, subject(s): COMPUTER SCIENCE, SOFTWARE ENGINEERING – COMPUTER SCIENCE, THEORY & METHODS. CORE: A. Citeseer: position 56/1221 (top 5%), impact 1.78. Average position: top 20%.
36. G. Puebla, F. Bueno, M. Hermenegildo. *A Framework for Assertion-based Debugging in Constraint Logic Programming (abstract)*. Proceedings of the International Conference on Principles and Practice of Constraint Programming (CP'98), LNCS, Num. 1520, pages 472–473, Springer-Verlag, October 1998.
CORE: A. CiteseerX position (ave) top 22%, impact (ave) 0.297. Average position: top 27%.
37. F. Bueno, M. Hermenegildo, U. Montanari, F. Rossi. *Partial Order and Contextual Net Semantics for Atomic and Locally Atomic CC Programs*. Science of Computer Programming, Vol. 30, pages 51–82, North-Holland, January 1998. Special CCP95 Workshop issue.
JCR: position (ave) top 42%, impact (ave) 0.80, subject(s): COMPUTER SCIENCE, SOFTWARE ENGINEERING – COMPUTER SCIENCE, SOFTWARE, GRAPHICS, PROGRAMMING. CORE: A. Citeseer: position 185/1221 (top 15%), impact 1.22. Average position: top 26%.
38. S. K. Debray, P. López-García, M. Hermenegildo, N.-W. Lin. *Lower Bound Cost Estimation for Logic Programs*. 1997 International Logic Programming Symposium, pages 291–305, MIT Press, Cambridge, MA, October 1997.
Citeseer: position 118/1221 (top 10%), impact 1.47. Average position: top 10%.
39. M. Codish, M. Bruynooghe, M. García de la Banda, M. Hermenegildo. *Exploiting Goal Independence in the Analysis of Logic Programs*. Journal of Logic Programming, Vol. 32, Num. 3, pages 247–261, Elsevier - North Holland, September 1997.
JCR: position (ave) top 36%, impact (ave) 1.20, subject(s): COMPUTER SCIENCE, SOFTWARE ENGINEERING – COMPUTER SCIENCE, THEORY & METHODS. CORE: A. Citeseer: position 56/1221 (top 5%), impact 1.78. Average position: top 20%.
40. S.K. Debray, P. López-García, M. Hermenegildo. *Non-Failure Analysis for Logic Programs*. 1997 International Conference on Logic Programming, pages 48–62, MIT Press, Cambridge, MA, June 1997.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.
41. M. García de la Banda, F. Bueno, M. Hermenegildo. *Towards Independent And-Parallelism in CLP*. Programming Languages: Implementation, Logics, and Programs, LNCS, Num. 1140, pages 77–91, Springer-Verlag, September 1996.
Citeseer: position 297/1221 (top 24%), impact 0.99. Average position: top 24%.

42. M. García de la Banda, M. Hermenegildo, M. Bruynooghe, V. Dumortier, G. Janssens, W. Simoens. *Global Analysis of Constraint Logic Programs*. ACM Transactions on Programming Languages and Systems, Vol. 18, Num. 5, pages 564–615, ACM, September 1996.
JCR: position (ave) top 22%, impact (ave) 1.27, subject(s): COMPUTER SCIENCE, SOFTWARE ENGINEERING – COMPUTER SCIENCE, SOFTWARE, GRAPHICS, PROGRAMMING. CORE: A*. Citeseer: position 41/1221 (top 3%), impact 1.92. Average position: top 10%.
43. G. Puebla, M. Hermenegildo. *Automatic Optimization of Dynamic Scheduling in Logic Programs*. Programming Languages: Implementation, Logics, and Programs, LNCS, Num. 1140, pages 475–476, Springer-Verlag, September 1996. Poster abstract.
Citeseer: position 297/1221 (top 24%), impact 0.99. Average position: top 24%.
44. F. Bueno, D. Cabeza, M. Hermenegildo, G. Puebla. *Global Analysis of Standard Prolog Programs*. European Symposium on Programming, LNCS, Num. 1058, pages 108–124, Springer-Verlag, April 1996.
CORE: A. Citeseer: position 87/1221 (top 7%), impact 1.58. Average position: top 20%.
45. P. López-García, M. Hermenegildo, S. K. Debray. *A Methodology for Granularity Based Control of Parallelism in Logic Programs*. Journal of Symbolic Computation, Special Issue on Parallel Symbolic Computation, Vol. 21, Num. 4–6, pages 715–734, Academic Press, 1996.
JCR: position (ave) top 54%, impact (ave) 0.64, subject(s): COMPUTER SCIENCE, THEORY & METHODS. CORE: A. Citeseer: position 362/1221 (top 30%), impact 0.85. Average position: top 35%.
46. M. Hermenegildo, D. Cabeza, M. Carro. *Using Attributed Variables in the Implementation of Concurrent and Parallel Logic Programming Systems*. Proc. of the Twelfth International Conference on Logic Programming, pages 631–645, MIT Press, June 1995.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.
47. M. Hermenegildo, G. Puebla, K. Marriott, P. Stuckey. *Incremental Analysis of Logic Programs*. International Conference on Logic Programming, pages 797–811, MIT Press, June 1995.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.
48. P. López-García, M. Hermenegildo. *Efficient Term Size Computation for Granularity Control*. International Conference on Logic Programming, pages 647–661, MIT Press, Cambridge, MA, June 1995.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.
49. M. Codish, A. Mulkers, M. Bruynooghe, M. García de la Banda, M. Hermenegildo. *Improving Abstract Interpretations by Combining Domains*. ACM Transactions on Programming Languages and Systems, Vol. 17, Num. 1, pages 28–44, ACM, January 1995.
JCR: position (ave) top 22%, impact (ave) 1.27, subject(s): COMPUTER SCIENCE, SOFTWARE ENGINEERING – COMPUTER SCIENCE, SOFTWARE, GRAPHICS, PROGRAMMING. CORE: A*. Citeseer: position 41/1221 (top 3%), impact 1.92. Average position: top 10%.
50. M. Hermenegildo, F. Rossi. *Strict and Non-Strict Independent And-Parallelism in Logic Programs: Correctness, Efficiency, and Compile-Time Conditions*. Journal of Logic Programming, Vol. 22, Num. 1, pages 1–45, Elsevier - North Holland, 1995.
JCR: position (ave) top 36%, impact (ave) 1.20, subject(s): COMPUTER SCIENCE, SOFTWARE ENGINEERING – COMPUTER SCIENCE, THEORY & METHODS. CORE: A. Citeseer: position 56/1221 (top 5%), impact 1.78. Average position: top 20%.
51. F. Bueno, M. García de la Banda, M. Hermenegildo. *Effectiveness of Global Analysis in Strict Independence-Based Automatic Program Parallelization*. International Symposium on Logic Programming, pages 320–336, MIT Press, November 1994.
Citeseer: position 118/1221 (top 10%), impact 1.47. Average position: top 10%.

52. D. Cabeza, M. Hermenegildo. *Extracting Non-Strict Independent And-Parallelism Using Sharing and Freeness Information*. 1994 International Static Analysis Symposium, LNCS, Num. 864, pages 297–313, Springer-Verlag, September 1994.
CORE: A. Citeseer: position 145/1221 (top 12%), impact 1.35. Average position: top 22%.
53. C. Carreras, C. López-Barrio, M. Hermenegildo. *Analytic Model of a Cache-only Memory Architecture*. Parallel Architectures and Languages Europe – PARLE'94, LNCS, Num. 817, pages 336–351, Springer-Verlag, July 1994.
Citeseer: position 491/1221 (top 40%), impact 0.67. Average position: top 40%.
54. M. Codish, M. García de la Banda, M. Bruynooghe, M. Hermenegildo. *Goal Dependent vs Goal Independent Analysis of Logic Programs*. Fifth International Conference on Logic Programming and Automated Reasoning, LNAI, Num. 822, pages 305–320, Springer-Verlag, July 1994.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.
55. G. Gupta, M. Hermenegildo, E. Pontelli, V. Santos-Costa. *ACE: And/Or-parallel Copying-based Execution of Logic Programs*. International Conference on Logic Programming, pages 93–110, MIT Press, June 1994.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.
56. U. Montanari, F. Rossi, F. Bueno, M. García de la Banda, M. Hermenegildo. *Towards a Concurrent Semantics-based Analysis of CC and CLP*. Principles and Practice of Constraint Programming, LNCS, Num. 874, pages 151–161, Springer-Verlag, May 1994.
CORE: A. CiteseerX position (ave) top 22%, impact (ave) 0.297. Average position: top 27%.
57. M. Hermenegildo, The CLIP Group. *Some Methodological Issues in the Design of CIAO - A Generic, Parallel, Concurrent Constraint System*. Principles and Practice of Constraint Programming, LNCS, Num. 874, pages 123–133, Springer-Verlag, May 1994.
CORE: A. CiteseerX position (ave) top 22%, impact (ave) 0.297. Average position: top 27%.
58. K. Marriott, M. García de la Banda, M. Hermenegildo. *Analyzing Logic Programs with Dynamic Scheduling*. 20th. Annual ACM Conf. on Principles of Programming Languages, pages 240–254, ACM, January 1994.
CORE: A. Citeseer: position 13/1221 (top 1%), impact 2.26. Average position: top 17%.
59. M. García de la Banda, M. Hermenegildo. *A Practical Approach to the Global Analysis of Constraint Logic Programs*. 1993 International Logic Programming Symposium, pages 437–455, MIT Press, October 1993.
Citeseer: position 118/1221 (top 10%), impact 1.47. Average position: top 10%.
60. M. García de la Banda, M. Hermenegildo, K. Marriott. *Independence in Constraint Logic Programs*. 1993 International Logic Programming Symposium, pages 130–146, MIT Press, Cambridge, MA, October 1993.
Citeseer: position 118/1221 (top 10%), impact 1.47. Average position: top 10%.
61. M. Carro, L. Gómez, M. Hermenegildo. *Some Paradigms for Visualizing Parallel Execution of Logic Programs*. 1993 International Conference on Logic Programming, pages 184–201, MIT Press, June 1993.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.
62. H. Kuchen, J. Moreno-Navarro, M. Hermenegildo. *Independent AND-Parallel Implementation of Narrowing*. Proc. 4th. Int'l Symposium on Programming Language Implementation and Logic Programming, LNCS, Num. 631, pages 24–38, Springer-Verlag, August 1992.
Citeseer: position 297/1221 (top 24%), impact 0.99. Average position: top 24%.

63. M. Hermenegildo, R. Warren, S. K. Debray. *Global Flow Analysis as a Practical Compilation Tool*. Journal of Logic Programming, Vol. 13, Num. 4, pages 349–367, Elsevier - North Holland, August 1992.
JCR: position (ave) top 36%, impact (ave) 1.20, subject(s): COMPUTER SCIENCE, SOFTWARE ENGINEERING – COMPUTER SCIENCE, THEORY & METHODS. CORE: A. Citeseer: position 56/1221 (top 5%), impact 1.78. Average position: top 20%.
64. K. Muthukumar, M. Hermenegildo. *Compile-time Derivation of Variable Dependency Using Abstract Interpretation*. Journal of Logic Programming, Vol. 13, Num. 2/3, pages 315–347, Elsevier - North-Holland, July 1992.
JCR: position (ave) top 36%, impact (ave) 1.20, subject(s): COMPUTER SCIENCE, SOFTWARE ENGINEERING – COMPUTER SCIENCE, THEORY & METHODS. CORE: A. Citeseer: position 56/1221 (top 5%), impact 1.78. Average position: top 20%.
65. G. Gupta, M. Hermenegildo. *Recomputation based Implementation of And-Or Parallel Prolog*. Proc. of the 1992 International Conference on Fifth Generation Computer Systems, pages 770–782, Institute for New Generation Computer Technology (ICOT), June 1992.
Citeseer: position 359/1221 (top 29%), impact 0.85. Average position: top 29%.
66. F. Bueno, M. Hermenegildo. *An Automatic Translation Scheme from Prolog to the Andorra Kernel Language*. Proc. of the 1992 International Conference on Fifth Generation Computer Systems, Vol. 2, pages 759–769, Institute for New Generation Computer Technology (ICOT), June 1992.
Citeseer: position 359/1221 (top 29%), impact 0.85. Average position: top 29%.
67. K. Shen, M. Hermenegildo. *A Simulation Study of Or- and Independent And-parallelism*. International Logic Programming Symposium, pages 135–151, MIT Press, October 1991.
Citeseer: position 118/1221 (top 10%), impact 1.47. Average position: top 10%.
68. G. Gupta, V. Santos-Costa, R. Yang, M. Hermenegildo. *IDIOM: Integrating Dependent And-, Independent And-, and Or-parallelism*. 1991 International Logic Programming Symposium, pages 152–166, MIT Press, October 1991.
Citeseer: position 118/1221 (top 10%), impact 1.47. Average position: top 10%.
69. F. Giannotti, M. Hermenegildo. *A Technique for Recursive Invariance Detection and Selective Program Specialization*. Proc. 3rd. Int'l Symposium on Programming Language Implementation and Logic Programming, LNCS, Num. 528, pages 323–335, Springer-Verlag, August 1991.
Citeseer: position 297/1221 (top 24%), impact 0.99. Average position: top 24%.
70. K. Muthukumar, M. Hermenegildo. *Combined Determination of Sharing and Freeness of Program Variables Through Abstract Interpretation*. International Conference on Logic Programming (ICLP 1991), pages 49–63, MIT Press, June 1991.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.
71. S. K. Debray, N.-W. Lin, M. Hermenegildo. *Task Granularity Analysis in Logic Programs*. Proc. of the 1990 ACM Conf. on Programming Language Design and Implementation, pages 174–188, ACM Press, June 1990.
CORE: A. Citeseer: position 3/1221 (top 0%), impact 2.89. Average position: top 17%.
72. K. Muthukumar, M. Hermenegildo. *The CDG, UDG, and MEL Methods for Automatic Compile-time Parallelization of Logic Programs for Independent And-parallelism*. Int'l. Conference on Logic Programming, pages 221–237, MIT Press, June 1990.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.

73. M. Hermenegildo, F. Rossi. *Non-Strict Independent And-Parallelism*. 1990 International Conference on Logic Programming, pages 237–252, MIT Press, Association for Logic Programming, June 1990.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.
74. M. Hermenegildo, K. Greene. *&-Prolog and its Performance: Exploiting Independent And-Parallelism*. 1990 International Conference on Logic Programming, pages 253–268, MIT Press, Association for Logic Programming, June 1990.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.
75. K. Muthukumar, M. Hermenegildo. *Determination of Variable Dependence Information at Compile-Time Through Abstract Interpretation*. 1989 North American Conference on Logic Programming, pages 166–189, MIT Press, October 1989.
Citeseer: position 138/1221 (top 11%), impact 1.38. Average position: top 11%.
76. M. Hermenegildo, F. Rossi. *On the Correctness and Efficiency of Independent And-Parallelism in Logic Programs*. 1989 North American Conference on Logic Programming, pages 369–390, MIT Press, October 1989.
Citeseer: position 138/1221 (top 11%), impact 1.38. Average position: top 11%.
77. K. Muthukumar, M. Hermenegildo. *Complete and Efficient Methods for Supporting Side Effects in Independent/Restricted And-parallelism*. 1989 International Conference on Logic Programming, pages 80–101, MIT Press, June 1989.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.
78. M. Hermenegildo, E. Tick. *Memory Performance of AND-Parallel Prolog on Shared-Memory Architectures*. Proceedings of the 17th International Conference on Parallel Processing, pages 17–22, IEEE, August 1988.
CORE: A. Citeseer: position 313/1221 (top 26%), impact 0.95. Average position: top 29%.
79. R. Warren, M. Hermenegildo, S. K. Debray. *On the Practicality of Global Flow Analysis of Logic Programs*. Fifth International Conference and Symposium on Logic Programming, pages 684–699, MIT Press, August 1988.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.
80. M. Hermenegildo. *Relating Goal Scheduling, Precedence, and Memory Management in AND-Parallel Execution of Logic Programs*. Fourth International Conference on Logic Programming, pages 556–575, MIT Press, University of Melbourne, May 1987.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.
81. M. Hermenegildo, R. I. Nasr. *Efficient Management of Backtracking in AND-parallelism*. Third International Conference on Logic Programming, LNCS, Num. 225, pages 40–55, Springer-Verlag, Imperial College, July 1986.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.
82. M. Hermenegildo. *An Abstract Machine for Restricted AND-parallel Execution of Logic Programs*. Third International Conference on Logic Programming, Lecture Notes in Computer Science, Num. 225, pages 25–40, Springer-Verlag, Imperial College, July 1986.
CORE: A. Citeseer: position 188/1221 (top 15%), impact 1.21. Average position: top 24%.
83. G. J. Lipovski, M. Hermenegildo. *B-LOG: A Branch and Bound Methodology for the Parallel Execution of Logic Programs*. 1985 IEEE International Conference on Parallel Processing, pages 560–568, IEEE Computer Society, August 1985.
CORE: A. Citeseer: position 313/1221 (top 26%), impact 0.95. Average position: top 29%.

1. J. F. Morales, M. V. Hermenegildo, R. Haemmerlé. *Modular Extensions for Modular (Logic) Languages*. 21th International Symposium on Logic-Based Program Synthesis and Transformation (LOPSTR'11), July 2011. To appear.
CORE: B. Citeseer: position 363/1221 (top 30%), impact 0.85. Average position: top 47%.
2. L. Scandolo, C. Kunz, G. Barthe, M.V. Hermenegildo. *Program Parallelization using Synchronized Pipelining*. Proceedings of the 19th International Symposium on Logic-based Program Synthesis and Transformation (LOPSTR'09), LNCS, Num. 6037, pages 173–187, Springer, 2010.
CORE: B. Citeseer: position 363/1221 (top 30%), impact 0.85. Average position: top 47%.
3. P. Chico de Guzmán, M. Carro, M. Hermenegildo. *Towards a Complete Scheme for Tabled Execution Based on Program Transformation*. 11th International Symposium on Practical Aspects of Declarative Languages (PADL'09), LNCS, Num. 5418, pages 224–238, Springer-Verlag, January 2009.
CORE: B. CiteseerX position (ave) top 23%, impact (ave) 0.230. Average position: top 44%.
4. M. Marron, M. Méndez-Lojo, M. Hermenegildo, D. Stefanovic, D. Kapur. *Sharing Analysis of Arrays, Collections, and Recursive Structures*. ACM WS on Program Analysis for Software Tools and Engineering (PASTE'08), ACM, November 2008.
CORE: B. CiteseerX position (ave) top 14%, impact (ave) 0.245. Average position: top 39%.
5. J. Morales, M. Carro, M. Hermenegildo. *Comparing Tag Scheme Variations Using an Abstract Machine Generator*. 10th Int'l. ACM SIGPLAN Symposium on Principles and Practice of Declarative Programming (PPDP'08), pages 32–43, ACM Press, July 2008.
CORE: B. CiteseerX position (ave) top 26%, impact (ave) 0.183. Average position: top 45%.
6. E. Mera, P. López-García, M. Carro, M. Hermenegildo. *Towards Execution Time Estimation in Abstract Machine-Based Languages*. 10th Int'l. ACM SIGPLAN Symposium on Principles and Practice of Declarative Programming (PPDP'08), pages 174–184, ACM Press, July 2008.
CORE: B. CiteseerX position (ave) top 26%, impact (ave) 0.183. Average position: top 45%.
7. M. Méndez-Lojo, M. Hermenegildo. *Precise Set Sharing Analysis for Java-style Programs*. 9th International Conference on Verification, Model Checking and Abstract Interpretation (VMCAI'08), LNCS, Num. 4905, pages 172–187, Springer-Verlag, January 2008.
CORE: B. CiteseerX position (ave) top 14%, impact (ave) 0.330. Average position: top 39%.
8. P. Chico de Guzmán, M. Carro, M. Hermenegildo, Claudio Silva, Ricardo Rocha. *An Improved Continuation Call-Based Implementation of Tabling*. 10th International Symposium on Practical Aspects of Declarative Languages (PADL'08), LNCS, Vol. 4902, pages 198–213, Springer-Verlag, January 2008.
CORE: B. CiteseerX position (ave) top 23%, impact (ave) 0.230. Average position: top 44%.
9. A. Casas, M. Carro, M. Hermenegildo. *Towards a High-Level Implementation of Execution Primitives for Non-restricted, Independent And-parallelism*. 10th International Symposium on Practical Aspects of Declarative Languages (PADL'08), LNCS, Vol. 4902, pages 230–247, Springer-Verlag, January 2008.
CORE: B. CiteseerX position (ave) top 23%, impact (ave) 0.230. Average position: top 44%.
10. P. Pietrzak, J. Correas, G. Puebla, M. Hermenegildo. *A Practical Type Analysis for Verification of Modular Prolog Programs*. ACM SIGPLAN 2008 Workshop on Partial Evaluation and Program Manipulation (PEPM'08), pages 61–70, ACM Press, January 2008.
CORE: B. Citeseer: position 65/1221 (top 5%), impact 1.74. Average position: top 35%.

11. M. Méndez-Lojo, J. Navas, M. Hermenegildo. *A Flexible (C)LP-Based Approach to the Analysis of Object-Oriented Programs*. 17th International Symposium on Logic-based Program Synthesis and Transformation (LOPSTR 2007), LNCS, Num. 4915, pages 154–168, Springer-Verlag, August 2007.
CORE: B. Citeseer: position 363/1221 (top 30%), impact 0.85. Average position: top 47%.
12. A. Casas, M. Carro, M. Hermenegildo. *Annotation Algorithms for Unrestricted Independent And-Parallelism in Logic Programs*. 17th International Symposium on Logic-based Program Synthesis and Transformation (LOPSTR'07), LNCS, Num. 4915, pages 138–153, Springer-Verlag, August 2007.
CORE: B. Citeseer: position 363/1221 (top 30%), impact 0.85. Average position: top 47%.
13. J.F. Morales, M. Carro, M. Hermenegildo. *Towards Description and Optimization of Abstract Machines in an Extension of Prolog*. Logic-Based Program Synthesis and Transformation (LOPSTR'06), LNCS, Num. 4407, pages 77–93, July 2007.
CORE: B. Citeseer: position 363/1221 (top 30%), impact 0.85. Average position: top 47%.
14. M. Marron, D. Stefanovic, M. Hermenegildo, D. Kapur. *Heap Analysis in the Presence of Collection Libraries*. ACM WS on Program Analysis for Software Tools and Engineering (PASTE'07), ACM, June 2007.
CORE: B. CiteseerX position (ave) top 14%, impact (ave) 0.245. Average position: top 39%.
15. P. Pietrzak, J. Correas, G. Puebla, M. Hermenegildo. *Context-Sensitive Multivariant Assertion Checking in Modular Programs*. 13th International Conference on Logic for Programming Artificial Intelligence and Reasoning (LPAR'06), LNCS, Num. 4246, pages 392–406, Springer-Verlag, November 2006.
CORE: A. CiteseerX position (ave) top 38%, impact (ave) 0.169. Average position: top 36%.
16. A. Casas, D. Cabeza, M. Hermenegildo. *A Syntactic Approach to Combining Functional Notation, Lazy Evaluation and Higher-Order in LP Systems*. The 8th International Symposium on Functional and Logic Programming (FLOPS'06), pages 142–162, April 2006.
CORE: A. Citeseer: position 468/1221 (top 38%), impact 0.69. Average position: top 36%.
17. C. Ochoa, G. Puebla, M. Hermenegildo. *Removing Superfluous Versions in Polyvariant Specialization of Prolog Programs*. 15th International Symposium on Logic-based Program Synthesis and Transformation (LOPSTR'05), LNCS, Num. 3901, pages 80–97, Springer-Verlag, April 2006.
CORE: B. Citeseer: position 363/1221 (top 30%), impact 0.85. Average position: top 47%.
18. J. Correas, G. Puebla, M. Hermenegildo, F. Bueno. *Experiments in Context-Sensitive Analysis of Modular Programs*. 15th International Symposium on Logic-based Program Synthesis and Transformation (LOPSTR'05), LNCS, Num. 3901, pages 163–178, Springer-Verlag, April 2006.
CORE: B. Citeseer: position 363/1221 (top 30%), impact 0.85. Average position: top 47%.
19. J. Navas, F. Bueno, M. Hermenegildo. *Efficient top-down set-sharing analysis using cliques*. Eight International Symposium on Practical Aspects of Declarative Languages, LNCS, Num. 2819, pages 183–198, Springer-Verlag, January 2006.
CORE: B. CiteseerX position (ave) top 23%, impact (ave) 0.230. Average position: top 44%.
20. G. Puebla, E. Albert, M. Hermenegildo. *Efficient Local Unfolding with Ancestor Stacks for Full Prolog*. 14th International Symposium on Logic-based Program Synthesis and Transformation (LOPSTR'04), LNCS, Num. 3573, pages 149–165, Springer-Verlag, August 2005.
CORE: B. Citeseer: position 363/1221 (top 30%), impact 0.85. Average position: top 47%.

21. P. López-García, F. Bueno, M. Hermenegildo. *Determinacy Analysis for Logic Programs Using Mode and Type Information*. Proceedings of the 14th International Symposium on Logic-based Program Synthesis and Transformation (LOPSTR'04), LNCS, Num. 3573, pages 19–35, Springer-Verlag, August 2005.
CORE: B. Citeseer: position 363/1221 (top 30%), impact 0.85. Average position: top 47%.
22. E. Albert, G. Puebla, M. Hermenegildo. *Abstraction-Carrying Code*. 11th International Conference on Logic for Programming Artificial Intelligence and Reasoning (LPAR 2004), LNAI, Num. 3452, pages 380–397, Springer-Verlag, March 2005.
CORE: A. CiteseerX position (ave) top 38%, impact (ave) 0.169. Average position: top 36%.
23. D. Cabeza, M. Hermenegildo, J. Lipton. *Hiord: A Type-Free Higher-Order Logic Programming Language with Predicate Abstraction*. Ninth Asian Computing Science Conference (ASIAN'04), LNCS, Num. 3321, pages 93–108, Springer-Verlag, December 2004.
CORE: B. CiteseerX position (ave) top 58%, impact (ave) 0.092. Average position: top 61%.
24. J. Morales, M. Carro, M. Hermenegildo. *Improving the Compilation of Prolog to C Using Moded Types and Determinism Information*. Proceedings of the Sixth International Symposium on Practical Aspects of Declarative Languages, Lecture Notes in Computer Science, Vol. 3057, pages 86–103, Springer-Verlag, June 2004.
CORE: B. CiteseerX position (ave) top 23%, impact (ave) 0.230. Average position: top 44%.
25. J. Correas, J. M. Gomez, M. Carro, D. Cabeza, M. Hermenegildo. *A Generic Persistence Model for CLP Systems (And Two Useful Implementations)*. Proceedings of the Sixth International Symposium on Practical Aspects of Declarative Languages, LNCS, Num. 3057, pages 104–119, Springer-Verlag, June 2004.
CORE: B. CiteseerX position (ave) top 23%, impact (ave) 0.230. Average position: top 44%.
26. F. Bueno, P. López-García, M. Hermenegildo. *Multivariant Non-Failure Analysis via Standard Abstract Interpretation*. 7th International Symposium on Functional and Logic Programming (FLOPS 2004), LNCS, Num. 2998, pages 100–116, Springer-Verlag, April 2004.
CORE: A. Citeseer: position 468/1221 (top 38%), impact 0.69. Average position: top 36%.
27. F. Bueno, M. García de la Banda, M. Hermenegildo, K. Marriott, G. Puebla, P. Stuckey. *A Model for Inter-module Analysis and Optimizing Compilation*. Logic-based Program Synthesis and Transformation, LNCS, Num. 2042, pages 86–102, Springer-Verlag, March 2001.
CORE: B. Citeseer: position 363/1221 (top 30%), impact 0.85. Average position: top 47%.
28. C. Carreras, M. Hermenegildo. *Grid-based Histogram Arithmetic for the Probabilistic Analysis of Functions*. Symposium on Abstraction, Reformulation and Approximation (SARA-2000), LNAI, Springer-Verlag, July 2000.
CORE: B. CiteseerX position (ave) top 43%, impact (ave) 0.092. Average position: top 54%.
29. G. Puebla, F. Bueno, M. Hermenegildo. *Combined Static and Dynamic Assertion-Based Debugging of Constraint Logic Programs*. Logic-based Program Synthesis and Transformation (LOPSTR'99), LNCS, Num. 1817, pages 273–292, Springer-Verlag, March 2000.
CORE: B. Citeseer: position 363/1221 (top 30%), impact 0.85. Average position: top 47%.
30. G. Puebla, M. Hermenegildo. *Abstract Specialization and its Application to Program Parallelization*. Logic Program Synthesis and Transformation, LNCS, Num. 1207, pages 169–186, Springer-Verlag, 1997.
CORE: B. Citeseer: position 363/1221 (top 30%), impact 0.85. Average position: top 47%.
31. M. García de la Banda, M. Hermenegildo, K. Marriott. *Independence in Dynamically Scheduled Logic Languages*. 1996 International Conference on Algebraic and Logic Programming, LNCS, Num. 1139, pages 47–61, Springer-Verlag, September 1996.
CORE: B. Citeseer: position 263/1221 (top 22%), impact 1.05. Average position: top 43%.

32. K. Shen, M. Hermenegildo. *Flexible Scheduling for Non-Deterministic, And-parallel Execution of Logic Programs*. Proceedings of EuroPar'96, LNCS, Num. 1124, pages 635–640, Springer-Verlag, August 1996.
Citeseer: position 491/1221 (top 40%), impact 0.67. Average position: top 40%.
33. M. Fernández, M. Carro, M. Hermenegildo. *IDRA (IDeal Resource Allocation): Computing Ideal Speedups in Parallel Logic Programming*. Proceedings of EuroPar'96, LNCS, Num. 1124, pages 724–734, Springer-Verlag, August 1996.
Citeseer: position 491/1221 (top 40%), impact 0.67. Average position: top 40%.
34. M. Hermenegildo, M. Carro. *Relating Data-Parallelism and And-Parallelism in Logic Programs*. Proceedings of EURO-PAR'95, LNCS, Num. 966, pages 27–42, Springer-Verlag, August 1995.
Citeseer: position 491/1221 (top 40%), impact 0.67. Average position: top 40%.
35. G. Puebla, M. Hermenegildo. *Implementation of Multiple Specialization in Logic Programs*. Proc. ACM SIGPLAN Symposium on Partial Evaluation and Semantics Based Program Manipulation, pages 77–87, ACM Press, June 1995.
CORE: B. Citeseer: position 65/1221 (top 5%), impact 1.74. Average position: top 35%.
36. E. Pontelli, G. Gupta, M. Hermenegildo. *&ACE: A High-Performance Parallel Prolog System*. International Parallel Processing Symposium, pages 564–572, IEEE Computer Society, IEEE Computer Society Technical Committee on Parallel Processing, April 1995.
CORE: A. CiteseerX position (ave) top 36%, impact (ave) 0.316. Average position: top 34%.
37. F. Bueno, M. Hermenegildo, U. Montanari, F. Rossi. *From Eventual to Atomic and Locally Atomic CC Programs: A Concurrent Semantics*. Fourth International Conference on Algebraic and Logic Programming, LNCS, Num. 850, pages 114–132, Springer-Verlag, September 1994.
CORE: B. Citeseer: position 263/1221 (top 22%), impact 1.05. Average position: top 43%.
38. M. Olmedilla, F. Bueno, M. Hermenegildo. *Automatic Exploitation of Non-Determinate Independent And-Parallelism in the Basic Andorra Model*. Logic Program Synthesis and Transformation, 1993, Workshops in Computing, pages 177–195, Springer-Verlag, July 1993.
CORE: B. Citeseer: position 363/1221 (top 30%), impact 0.85. Average position: top 47%.
39. M. Codish, A. Mulkers, M. Bruynooghe, M. García de la Banda, M. Hermenegildo. *Improving Abstract Interpretations by Combining Domains*. Proc. ACM SIGPLAN Symposium on Partial Evaluation and Semantics Based Program Manipulation, pages 194–206, ACM, June 1993.
CORE: B. Citeseer: position 65/1221 (top 5%), impact 1.74. Average position: top 35%.

Books and Monographs

1. M. Hermenegildo, T. Schaub. *Theory and Practice of Logic Programming. 26th Int'l. Conference on Logic Programming (ICLP'10) Special Issue*. Vol. 10 (4–6), pages 361–778, Cambridge University Press, July 2010.
2. G. Barthe, M. Hermenegildo. *Verification, Model Checking, and Abstract Interpretation, 11th International Conference, VMCAI 2010*. LNCS, Num. 5944, Springer, January 2010.
3. J. Garrigue, M. Hermenegildo. *Functional and Logic Programming, 9th International Symposium, FLOPS'08*. LNCS, Num. 4989, Springer, April 2008.
4. M. Hermenegildo. *ACM SIGPLAN-Intel Workshop on Declarative Aspects of Multicore Programming, DAMP'08, Informal Proceedings*. January 2008.
5. M. Hermenegildo, D. Cabeza. *Practical Aspects of Declarative Languages – 7th. International Symposium, PADL 2005*. LNCS, Num. 3350, 278 pages, Springer-Verlag, January 2005.

6. M. Hermenegildo, G. Puebla. *Static Analysis – 9th. International Symposium*. LNCS, Num. 2477, 528 pages, Springer-Verlag, September 2002.
7. L. Sterling, L. Naish, M. Hermenegildo. *Theory and Practice of Logic Programming – Special Issue: Logic Programming and the INTERNET*. Vol. 1, Num. 3, 131 pages, Cambridge U. Press, May 2001.
8. P. Deransart, M. Hermenegildo, J. Maluszynski. *Analysis and Visualization Tools for Constraint Programming*. LNCS, Num. 1870, 350 pages, Springer-Verlag, September 2000.
9. P. Tarau, K. De Bosschere, M. Hermenegildo. *Proceedings of the 2nd Workshop on Logic Programming Tools for INTERNET Applications*. 140 pages, K.U. Leuven, July 1997. Available from <http://www.cliplab.org/lpnet/proceedings97/index.html>.
10. P. Tarau, A. Davison, K. De Bosschere, M. Hermenegildo. *Proceedings of the 1st Workshop on Logic Programming Tools for INTERNET Applications*. 122 pages, U. of Moncton, Canada, September 1996. Available from <http://www.cliplab.org/miscdocs/lp-internet/archive.html>.
11. P. López-García, M. Hermenegildo. *Proceedings of the Third COMPULOG-NET Area Workshop on Parallelism and Implementation Technologies*. 108 pages, U. of Utrecht, September 1995.
12. M. Hermenegildo, D. Swierstra. *Programming Languages: Implementations, Logics, and Programs – Proc. of the 1995 Conference*. LNCS, Num. 982, 466 pages, Springer-Verlag, September 1995.
13. M. García de la Banda, M. Hermenegildo. *Proceedings of the Second COMPULOG-NET Area Workshop on Parallelism and Implementation Technologies*. 96 pages, FIM/UPM, Madrid, Spain, September 1994.
14. M. Hermenegildo, J. Penjam. *Programming Language Implementation and Logic Programming – Proc. of the 1994 Conference*. LNCS, Num. 844, 468 pages, Springer-Verlag, September 1994.
15. M. Bruynooghe, S.K. Debray, M. Hermenegildo, M. Maher. *The Journal of Logic Programming – Special Issue: Ten Years of Logic Programming*. Vol. 19/20, 714 pages, Elsevier - North-Holland, May 1994.
16. F. Bueno, M. Hermenegildo, J. Moreno-Navarro. *Proceedings of the First COMPULOG-NET Area Workshop on Parallelism and Implementation Technologies*. 78 pages, FIM/UPM, Madrid, Spain, May 1993.
17. M. Hermenegildo, J. Moreno-Navarro. *Actas del Primer Congreso Nacional de Programación Declarativa*. 398 pages, FIM/UPM, Madrid, Spain, September 1992.
18. S. K. Debray, M. Hermenegildo. *Logic Programming – Proc. of the North American Conference*. 850 pages, MIT Press, Cambridge, MA, October 1990.
19. M. Hermenegildo. *Proc. of the Workshop on Future Directions in Logic Programming*. 210 pages, MCC, Austin, TX, April 1987.
20. M. Hermenegildo. *An Abstract Machine Based Execution Model for Computer Architecture Design and Efficient Implementation of Logic Programs in Parallel*. Ph.D. Thesis, Dept. of Electrical and Computer Engineering (Dept. of Computer Science TR-86-20), University of Texas at Austin, Austin, Texas 78712, 244 pages, UMI, Boston, MA and U. of Texas, August 1986.
21. M. Hermenegildo. *Computational Models for Sequential and Parallel Execution of Logic Programs on a Reconfigurable Architecture*. Ms. Thesis, The University of Texas at Austin, ENS Bldg. 515, Austin, TX 78712, 122 pages, June 1984.

1. M. V. Hermenegildo, F. Bueno, M. Carro, P. López-García, R. Haemmerlé, E. Mera, J. F. Morales, G. Puebla. *An Overview of the Ciao System*. Proc. of RuleML-Europe 2011, LNCS, Num. 6826, pages 2–3, Springer-Verlag, July 2011. (abstract of invited talk).
2. M. Hermenegildo, T. Schaub. *Introduction to the 26th Int'l. Conference on Logic Programming (ICLP'10) Special Issue*. Vol. 10, Num. 4–6, pages 361–364, Cambridge University Press, July 2010.
3. M. Hermenegildo, T. Schaub. *Introduction to Technical Communications of the 26th Int'l. Conference on Logic Programming (ICLP'10)*. Technical Communications of the 26th Int'l. Conference on Logic Programming (ICLP'10), Leibniz International Proceedings in Informatics (LIPIcs), Vol. 7, pages 8–11, Schloss Dagstuhl–Leibniz-Zentrum fuer Informatik, July 2010.
4. M. Hermenegildo, F. Bueno, A. Casas, J. Navas, E. Mera, M. Carro, P. López-García. *Automatic Granularity-Aware Parallelization of Programs with Predicates, Functions, and Constraints*. DAMP'07, ACM SIGPLAN Workshop on Declarative Aspects of Multicore Programming, pages 1–1, ACM, January 2007. (abstract of invited talk).
5. G. Puebla, E. Albert, P. Arenas, M. Hermenegildo. *On Abstraction-Carrying Code and Certificate-Size Reduction*. 1st International Workshop on Emerging Applications of Abstract Interpretation (EAAI 2006), 15 pages, March 2006.
6. G. Puebla, E. Albert, M. Hermenegildo. *Abstract Interpretation-based Verification/Certification in the CiaoPP System*. Mobile Code Safety and Program Verification Using Computational Logic Tools (MoveLog'05), 9 pages, October 2005.
7. M. Hermenegildo, E. Albert, P. López-García, G. Puebla. *Abstraction Carrying Code and Resource-Awareness*. 7th ACM-SIGPLAN International Symposium on Principles and Practice of Declarative Programming (PPDP'05), pages 1–11, ACM Press, 2005.
8. M. Hermenegildo, E. Albert, P. López-García, G. Puebla. *Some Techniques for Automated, Resource-Aware Distributed and Mobile Computing in a Multi-Paradigm Programming System*. Proc. of EURO–PAR 2004, LNCS, Num. 3149, pages 21–37, Springer-Verlag, August 2004.
9. G. Puebla, M. Hermenegildo. *Abstract Specialization and its Applications*. ACM Partial Evaluation and Semantics based Program Manipulation (PEPM'03), pages 29–43, ACM Press, June 2003. Invited talk.
10. M. Hermenegildo, G. Puebla, F. Bueno, P. López-García. *Program Development Using Abstract Interpretation (and The Ciao System Preprocessor)*. 10th International Static Analysis Symposium (SAS'03), LNCS, Num. 2694, pages 127–152, Springer-Verlag, June 2003.
11. M. Hermenegildo, G. Puebla, F. Bueno, P. López-García. *Abstract Verification and Debugging of Constraint Logic Programs*. Recent Advances in Constraints, LNCS, Num. 2627, pages 1–14, Springer-Verlag, January 2003.
12. M. Hermenegildo, F. Bueno, G. Puebla, P. López-García. *Program Debugging and Validation Using Semantic Approximations and Partial Specifications*. 29 th. International Colloquium on Automata, Languages, and Programming (ICALP), LNCS, Num. 2380, pages 69–72, Springer-Verlag, July 2002.
13. M. Hermenegildo. *Parallelizing Irregular and Pointer-Based Computations Automatically: Results in Logic and Constraint Programming*. Constructive Methods for Parallel Programming, Advances in Computation: Theory and Practice, 1 pages, Nova Science, 2002. (abstract of invited talk).
14. F. Bueno, D. Cabeza, M. Carro, J. Correias, J. Gómez, M. Hermenegildo, P. López, G. Puebla, C. Vaucheret. *Agent Programming in Ciao Prolog*. 10 th. Portuguese Conference on Artificial Intelligence (EPIA), LNAI, Num. 2258, 2 pages, Springer-Verlag, December 2001.

15. M. Hermenegildo, F. Bueno, G. Puebla, P. López-García. *Program Analysis, Debugging and Optimization Using the Ciao System Preprocessor*. 1999 Int'l. Conference on Logic Programming, pages 52–66, MIT Press, November 1999.
16. M. Hermenegildo, The CLIP Group. *Programming with Global Analysis*. Proceedings of ILPS'97, pages 49–52, MIT Press, October 1997. (abstract of invited talk).
17. M. Hermenegildo. *Automatic Parallelization of Irregular and Pointer-Based Computations: Perspectives from Logic and Constraint Programming*. Proceedings of EUROPAR'97, LNCS, Vol. 1300, pages 31–46, Springer-Verlag, August 1997.
18. F. Bueno, P. Deransart, W. Drabent, G. Ferrand, M. Hermenegildo, J. Maluszynski, G. Puebla. *On the Role of Semantic Approximations in Validation and Diagnosis of Constraint Logic Programs*. Proc. of the 3rd. Int'l Workshop on Automated Debugging–AADEBUG'97, pages 155–170, U. of Linköping Press, May 1997.
19. M. Hermenegildo, G. Puebla. *Applying Multiple Abstract Specialization to Program Parallelization (abstract)*. Partial Evaluation–Dagstuhl Seminar Report, Num. 134, 1 pages, IBFI –Dagstuhl, February 1996.
20. S.K. Debray, P. López-García, M. Hermenegildo, N.-W. Lin. *Estimating the Computational Cost of Logic Programs*. Static Analysis Symposium, SAS'94, LNCS, Num. 864, pages 255–265, Springer-Verlag, September 1994.
21. M. García de la Banda, M. Hermenegildo. *A Practical Application of Sharing and Freeness Inference*. 1992 Workshop on Static Analysis WSA'92, BIGRE, Num. 81–82, pages 118–125, IRISA-Beaulieu, September 1992.

Articles in Books and Other Collections

1. M. V. Hermenegildo, F. Bueno, M. Carro, P. López, J.F. Morales, G. Puebla. *An Overview of The Ciao Multiparadigm Language and Program Development Environment and its Design Philosophy*. Festschrift for Ugo Montanari, LNCS, Num. 5065, pages 209–237, Springer-Verlag, June 2008.
2. G. Puebla, J. Correas, M. Hermenegildo, F. Bueno, M. García de la Banda, K. Marriott, P. J. Stuckey. *A Generic Framework for Context-Sensitive Analysis of Modular Programs*. Program Development in Computational Logic, A Decade of Research Advances in Logic-Based Program Development, LNCS, Num. 3049, pages 234–261, Springer-Verlag, August 2004.
3. M. Carro, M. Hermenegildo. *Visualization Designs for Constraint Logic Programming*. Vol. 2, Num. 2, pages 27–34, Swiss Informatics Societies, April 2001. Also in UPGRADE. Available through <http://www.svifsi.ch/revue/>.
4. M. Carro, M. Hermenegildo. *Diseño de visualizaciones para programación lógica con restricciones*. Num. 150, pages 31–37, Asociación de Técnicos en Informática, March 2001.
5. M. Carro, M. Hermenegildo. *Tools for Search Tree Visualization: The APT Tool*. Analysis and Visualization Tools for Constraint Programming, LNCS, Num. 1870, pages 237–252, Springer-Verlag, September 2000.
6. G. Puebla, F. Bueno, M. Hermenegildo. *A Generic Preprocessor for Program Validation and Debugging*. Analysis and Visualization Tools for Constraint Programming, LNCS, Num. 1870, pages 63–107, Springer-Verlag, September 2000.
7. G. Puebla, F. Bueno, M. Hermenegildo. *An Assertion Language for Constraint Logic Programs*. Analysis and Visualization Tools for Constraint Programming, LNCS, Num. 1870, pages 23–61, Springer-Verlag, September 2000.

8. P. Deransart, M. Hermenegildo, J. Maluszynski. *Debugging of Constraint Programs: The DiS-CiPI Methodology and Tools*. Analysis and Visualization Tools for Constraint Programming, LNCS, Num. 1870, pages 1–20, Springer-Verlag, September 2000.
9. M. Carro, M. Hermenegildo. *Tools for Constraint Visualization: The VIFID/TRIFID Tool*. Analysis and Visualization Tools for Constraint Programming, LNCS, Num. 1870, pages 253–272, Springer-Verlag, September 2000.
10. M. Hermenegildo, G. Puebla, F. Bueno. *Using Global Analysis, Partial Specifications, and an Extensible Assertion Language for Program Validation and Debugging*. The Logic Programming Paradigm: a 25–Year Perspective, pages 161–192, Springer-Verlag, July 1999.
11. M. Hermenegildo, F. Bueno, D. Cabeza, M. Carro, M. García de la Banda, P. López-García, G. Puebla. *The CIAO Multi-Dialect Compiler and System: An Experimentation Workbench for Future (C)LP Systems*. Parallelism and Implementation of Logic and Constraint Logic Programming, pages 65–85, Nova Science, April 1999.
12. K. Shen, M. Hermenegildo. *Divided We Stand: Parallel Distributed Stack Memory Management*. Implementations of Logic Programming Systems, pages 185–203, Kluwer Academic Publishers, 1994.
13. M. Hermenegildo. *The Role of Computational Logic as a Hinge Paradigm among Deduction, Problem Solving, Programming, and Parallelism*. Knowledge Oriented Software Design, pages 207–221, North-Holland, September 1993.
14. M. Hermenegildo. *Research on Parallel Logic Language Implementation and Architecture at ICOT*. The ICOT Research Exchange Report, Vol. 2, pages 157–174, Institute for New Generation Computer Technology (ICOT), June 1992.
15. M. Hermenegildo. *Fundamentos y Aplicación Automática del Paralelismo en Inteligencia Artificial*. Inteligencia Artificial: Fundamentos Teóricos y Aplicaciones, pages 127–153, Real Academia de Ciencias Exactas, Físicas y Naturales, March 1992.

Articles in Third-Level (or Non-Indexed) Refereed Conferences and Journals

1. P. Chico de Guzmán, A. Casas, M. Carro, M. Hermenegildo. *Parallel Backtracking with Answer Memoing for Independent And-Parallelism*. Theory and Practice of Logic Programming, 27th Int'l. Conference on Logic Programming (ICLP'11) Special Issue, Vol. 11, Num. 4–5, pages 555–574, Cambridge U. Press, July 2011. <http://arxiv.org/abs/1107.4724>.
JCR: position (ave) top 36%, impact (ave) 1.20, subject(s): COMPUTER SCIENCE, SOFTWARE ENGINEERING – COMPUTER SCIENCE, THEORY & METHODS. CORE: A. Citeseer: position 494/1221 (top 40%), impact 0.66. Average position: top 32%.
2. D. Ivanović, M. Carro, M. Hermenegildo. *Automated Attribute Inference in Complex Service Workflows Based on Sharing Analysis*. Proceedings of the 8th IEEE Conference on Services Computing SCC 2011, 8 pages, July 2011. To Appear.
CORE: A. Average position: top 33%.
3. E. Mera, T. Trigo, P. López-García, M. Hermenegildo. *Profiling for Run-Time Checking of Computational Properties and Performance Debugging*. Practical Aspects of Declarative Languages (PADL'11), LNCS, Vol. 6539, pages 38–53, Springer-Verlag, January 2011.
CORE: B. CiteseerX position (ave) top 23%, impact (ave) 0.230. Average position: top 44%.
4. R. Haemmerlé, P. López, M. Hermenegildo. *CLP Projection for Constraint Handling Rules*. Proceedings of the 13th International ACM SIGPLAN Conference on Principles and Practice of Declarative Programming, ACM Press, 2011.
CORE: B. CiteseerX position (ave) top 26%, impact (ave) 0.183. Average position: top 45%.

5. D. Ivanović, M. Carro, M. Hermenegildo. *Automatic Fragment Identification in Workflows Based on Sharing Analysis*. Service-Oriented Computing – ICSSOC 2010, LNCS, Num. 6470, 15 pages, Springer Verlag, 2010.
CORE: A. CiteseerX position (ave) top 18%, impact (ave) 0.163. Average position: top 26%.
6. A. Stivala, P. J. Stuckey, M. García de la Banda, M. Hermenegildo, A. Wirth. *Lock-free Parallel Dynamic Programming*. Journal of Parallel and Distributed Computing, Vol. 70, pages 839–848, Elsevier, 2010.
JCR: position (ave) top 57%, impact (ave) 0.62, subject(s): COMPUTER SCIENCE, THEORY & METHODS. CORE: A*. Citeseer: position 356/1221 (top 29%), impact 0.86. Average position: top 30%.
7. P. López-García, F. Bueno, M. Hermenegildo. *Automatic Inference of Determinacy and Mutual Exclusion for Logic Programs Using Mode and Type Information*. New Generation Computing, Vol. 28, Num. 2, pages 117–206, Ohmsha, Ltd. and Springer, 2010.
JCR: position (ave) top 61%, impact (ave) 0.54, subject(s): COMPUTER SCIENCE, HARDWARE & ARCHITECTURE – COMPUTER SCIENCE, THEORY & METHODS. CORE: B. Citeseer: position 305/1221 (top 25%), impact 0.97. Average position: top 50%.
8. D. Ivanović, M. Carro, M. Hermenegildo. *Towards Data-Aware QoS-Driven Adaptation for Service Orchestrations*. Proceedings of the 2010 IEEE International Conference on Web Services (ICWS 2010), Miami, FL, USA, 5-10 July 2010, IEEE, 2010.
CORE: A. Average position: top 33%.
9. M. Marron, D. Kapur, D. Stefanovic, M. Hermenegildo. *Identification of Heap-Carried Data Dependence Via Explicit Store Heap Models*. 21st Int'l. WS on Languages and Compilers for Parallel Computing (LCPC'08), LNCS, Springer-Verlag, August 2008.
10. E. Albert, G. Puebla, M. Hermenegildo. *Abstraction-Carrying Code: A Model for Mobile Code Safety*. New Generation Computing, Vol. 26, Num. 2, pages 171–204, March 2008.
JCR: position (ave) top 61%, impact (ave) 0.54, subject(s): COMPUTER SCIENCE, HARDWARE & ARCHITECTURE – COMPUTER SCIENCE, THEORY & METHODS. CORE: B. Citeseer: position 305/1221 (top 25%), impact 0.97. Average position: top 50%.
11. E. Mera, P. López-García, G. Puebla, M. Carro, M. Hermenegildo. *Combining Static Analysis and Profiling for Estimating Execution Times*. Ninth International Symposium on Practical Aspects of Declarative Languages (PADL07), LNCS, Num. 4354, pages 140–154, Springer-Verlag, January 2007.
CORE: B. CiteseerX position (ave) top 23%, impact (ave) 0.230. Average position: top 44%.
12. E. Albert, G. Puebla, M. Hermenegildo. *Experiments in Abstract Interpretation-based Code Certification for Pervasive Systems*. Proc. of 2004 IEEE Conference on Systems, Man & Cybernetics (Special Session on Correctness and Reliability for Pervasive/Ubiquitous Computing), 6 pages, IEEE, October 2004.
CORE: B. CiteseerX position (ave) top 92%, impact (ave) 0.010. Average position: top 78%.
13. G. Smedbäck, M. Carro, M. Hermenegildo. *Interfacing Prolog and VRML and its Application to Constraint Visualization*. The Practical Application of Constraint Technologies and Logic programming, pages 453–471, The Practical Application Company, April 1999.
14. M. Hermenegildo. *Some Challenges for Constraint Programming*. The Constraints Journal, Vol. 2, Num. 1, pages 63–69, Kluwer Academic Publishers, 1997. Special issue on strategic directions in constraint programming.
JCR: position (ave) top 57%, impact (ave) 0.83, subject(s): COMPUTER SCIENCE, ARTIFICIAL INTELLIGENCE – COMPUTER SCIENCE, THEORY & METHODS. CORE: A. Citeseer: position 327/1221 (top 27%), impact 0.93. Average position: top 35%.

15. P. Van Hentenryck, V. Saraswat, A. Borning, A. Brodski, P. Codognet, R. Dechter, M. Dincbas, E. Freuder, M. Hermenegildo, J. Jaffar, S. Kasif, J.-L. Lassez, D. McAllester, Ken McAloon, A. Macworth, U. Montanari, W. Older, J.-F. Puget, R. Ramakrishnan, F. Rossi, G. Smolka, R. Wachter. *Strategic Directions in Constraint Programming*. ACM Computing Surveys, Vol. 28, Num. 4, pages 701–726, ACM, December 1996. 50th Anniversary Issue on Strategic Directions in Computer Research.
JCR: position (ave) top 11%, impact (ave) 4.59, subject(s): COMPUTER SCIENCE, THEORY & METHODS. CORE: A*. Citeseer: position 195/1221 (top 16%), impact 1.19. Average position: top 11%.
16. G. Puebla, M. Hermenegildo. *Optimized Algorithms for the Incremental Analysis of Logic Programs*. International Static Analysis Symposium (SAS 1996), LNCS, Num. 1145, pages 270–284, Springer-Verlag, September 1996.
CORE: A. Citeseer: position 145/1221 (top 12%), impact 1.35. Average position: top 22%.
17. M. Hermenegildo, M. Carro. *Relating Data-Parallelism and (And-) Parallelism in Logic Programs*. The Computer Languages Journal, Vol. 22, Num. 2/3, pages 143–163, Elsevier Science, July 1996.
JCR: position (ave) top 73%, impact (ave) 0.34, subject(s): COMPUTER SCIENCE, SOFTWARE ENGINEERING – COMPUTER SCIENCE, SOFTWARE, GRAPHICS, PROGRAMMING. CORE: C. Citeseer: position 630/1221 (top 52%), impact 0.49. Average position: top 75%.
18. E. Pontelli, G. Gupta, D. Tang, M. Carro, M. Hermenegildo. *Improving the Efficiency of Nondeterministic And-parallel Systems*. The Computer Languages Journal, Vol. 22, Num. 2/3, pages 115–142, Pergamon/Elsevier, July 1996.
JCR: position (ave) top 73%, impact (ave) 0.34, subject(s): COMPUTER SCIENCE, SOFTWARE ENGINEERING – COMPUTER SCIENCE, SOFTWARE, GRAPHICS, PROGRAMMING. CORE: C. Citeseer: position 630/1221 (top 52%), impact 0.49. Average position: top 75%.
19. G. Puebla, M. Hermenegildo. *Optimized Algorithms for the Incremental Analysis of Logic Programs*. International Static Analysis Symposium (SAS 1996), LNCS, Num. 1145, pages 270–284, Springer-Verlag, 1996.
CORE: A. Citeseer: position 145/1221 (top 12%), impact 1.35. Average position: top 22%.
20. K. Shen, M. Hermenegildo. *High-level Characteristics of Or- and Independent And-parallelism in Prolog*. Int'l. Journal of Parallel Programming, Vol. 24, Num. 5, pages 433–478, Plenum Press, 1996.
JCR: position (ave) top 83%, impact (ave) 0.34, subject(s): COMPUTER SCIENCE, THEORY & METHODS. CORE: A. CiteseerX position (ave) top 28%, impact (ave) 0.210. Average position: top 43%.
21. F. Bueno, M. García de la Banda, M. Hermenegildo. *A Comparative Study of Methods for Automatic Compile-time Parallelization of Logic Programs*. First International Symposium on Parallel Symbolic Computation, PASCO'94, pages 63–73, World Scientific Publishing Company, September 1994.
22. P. López-García, M. Hermenegildo, S.K. Debray. *Towards Granularity Based Control of Parallelism in Logic Programs*. Proc. of First International Symposium on Parallel Symbolic Computation, PASCO'94, pages 133–144, World Scientific, September 1994.
23. G. Gupta, M. Hermenegildo, V. Santos-Costa. *And-Or Parallel Prolog: A Recomputation Based Approach*. New Generation Computing, Vol. 11, Num. 3–4, pages 297–323, Springer Verlag, 1993.
JCR: position (ave) top 61%, impact (ave) 0.54, subject(s): COMPUTER SCIENCE, HARDWARE & ARCHITECTURE – COMPUTER SCIENCE, THEORY & METHODS. CORE: B. Citeseer: position 305/1221 (top 25%), impact 0.97. Average position: top 50%.

24. M. Hermenegildo, K. Greene. *The &-Prolog System: Exploiting Independent And-Parallelism*. New Generation Computing, Vol. 9, Num. 3,4, pages 233–257, Springer Verlag, 1991.
JCR: position (ave) top 61%, impact (ave) 0.54, subject(s): COMPUTER SCIENCE, HARDWARE & ARCHITECTURE – COMPUTER SCIENCE, THEORY & METHODS. CORE: B. Cite-seer: position 305/1221 (top 25%), impact 0.97. Average position: top 50%.
25. M. Hermenegildo. *Bridging the Performance Gap between Declarative and Imperative Languages*. 4th. International Symposium on Knowledge Engineering, pages 91–104, Xerox Corp., May 1990.
26. M. Hermenegildo, E. Tick. *Memory Referencing Characteristics and Caching Performance of AND-Parallel Prolog on Shared-Memory Architectures*. New Generation Computing, Vol. 7, Num. 1, pages 37–58, Springer Verlag, October 1989.
JCR: position (ave) top 61%, impact (ave) 0.54, subject(s): COMPUTER SCIENCE, HARDWARE & ARCHITECTURE – COMPUTER SCIENCE, THEORY & METHODS. CORE: B. Cite-seer: position 305/1221 (top 25%), impact 0.97. Average position: top 50%.
27. A. Guzmán, M. Hermenegildo. *Constructs and Evaluation Strategies for Intelligent Speculative Parallelism – Armageddon Revisited*. 1988 ACM Computer Science Conference, pages 106–115, ACM, February 1988.
28. M. Hermenegildo, R. Warren. *Designing a High-Performance Parallel Logic Programming System*. Computer Architecture News, Special Issue on Parallel Symbolic Programming, Vol. 15, Num. 1, pages 43–53, ACM, March 1987.

Publications in Refereed Workshops

1. F. Bueno, M. García de la Banda, M. V. Hermenegildo, P. López-García, E. Mera, P. J. Stuckey. *Towards Resource Usage Analysis of MiniZinc Models*. MiniZinc Workshop (MZN'11), 15 pages, September 2011. To appear.
2. P. Lopez-Garcia, L. Darmawan, F. Bueno, M. Hermenegildo. *Interval-based Resource Usage Verification: Formalization and Prototype*. 2nd International Workshop on Foundational and Practical Aspects of Resource Analysis (FOPARA'2011), May 2011.
3. M. V. Hermenegildo, F. Bueno, M. Carro, P. López, E. Mera, J.F. Morales, G. Puebla. *The Ciao Approach to the Dynamic vs. Static Language Dilemma*. Proceedings for the International Workshop on Scripts to Programs, STOP'11, 4 pages, ACM, 2011.
4. D. Ivanović, M. Carro, M. Hermenegildo. *An Initial Proposal for Data-Aware Resource Analysis of Orchestrations with Applications to Predictive Monitoring*. International Workshops, ICSSOC/ServiceWave 2009, Revised Selected Papers, LNCS, Num. 6275, Springer, September 2010.
5. D. Ivanović, M. Carro, M. Hermenegildo. *An Initial Proposal for Data-Aware Resource Analysis of Orchestrations with Applications to Proactive Monitoring*. Pre-proceedings of the 2nd Workshop on Monitoring, Adaptation and Beyond (MONA+), November 2009.
6. L. Scandolo, C. Kunz, G. Barthe, M.V. Hermenegildo. *Program Parallelization using Synchronized Pipelining*. Pre-proceedings of the 19th International Symposium on Logic-based Program Synthesis and Transformation (LOPSTR'09), September 2009.
7. D. Ivanović, J.F. Morales, M. Carro, M. Hermenegildo. *Towards Structured State Threading in Prolog*. CICLOPS 2009, 15 pages, July 2009.
8. J. Navas, M. Méndez-Lojo, M. Hermenegildo. *User-Definable Resource Usage Bounds Analysis for Java Bytecode*. Proceedings of the Workshop on Bytecode Semantics, Verification, Analysis and Transformation (BYTECODE'09), Electronic Notes in Theoretical Computer Science, Vol. 253, Num. 5, pages 6–86, Elsevier - North Holland, March 2009.

9. P. Chico de Guzmán, M. Carro, M. Hermenegildo. *A Program Transformation for Continuation Call-Based Tabled Execution*. Colloquium on Implementation of Constraint and LOGic Programming Systems (ICLP associated workshop), 15 pages, University of Udine, December 2008.
10. M. Méndez-Lojo, O. Lhoták, M. Hermenegildo. *Efficient Set Sharing using ZBDDs*. 21st Int'l. WS on Languages and Compilers for Parallel Computing (LCPC'08), LNCS, Springer-Verlag, August 2008.
11. E. Trias, J. Navas, E. S. Ackley, S. Forrest, M. Hermenegildo. *Two Efficient Representations for Set-Sharing Analysis in Logic Programs*. 17th International Workshop on Functional and (Constraint) Logic Programming, WFLP'08, 15 pages, July 2008.
12. J. Navas, M. Méndez-Lojo, M. Hermenegildo. *Safe Upper-bounds Inference of Energy Consumption for Java Bytecode Applications*. The Sixth NASA Langley Formal Methods Workshop (LFM 08), April 2008. Extended Abstract.
13. P. Chico de Guzmán, M. Carro, M. Hermenegildo, Claudio Silva, Ricardo Rocha. *Some Improvements over the Continuation Call Tabling Implementation Technique*. Colloquium on Implementation of Constraint and LOGic Programming Systems (ICLP associated workshop), 15 pages, Universidade do Porto, September 2007.
14. A. Casas, M. Carro, M. Hermenegildo. *Towards High-Level Execution Primitives for And-Parallelism: Preliminary Results*. Colloquium on Implementation of Constraint and LOGic Programming Systems (CICLOPS'07, ICLP associated workshop), 15 pages, U. of Evora, September 2007.
15. J. Navas, M. Méndez-Lojo, M. Hermenegildo. *An Efficient, Context and Path Sensitive Analysis Framework for Java Programs*. 9th Workshop on Formal Techniques for Java-like Programs FTfJP 2007, 12 pages, July 2007.
16. A. Casas, M. Carro, M. Hermenegildo. *Towards A High-Level Implementation of Flexible Parallelism Primitives for Symbolic Languages*. Parallel Symbolic Computation (PASCO'07), 2 pages, ACM Press, July 2007. Extended Abstract.
17. M. Hermenegildo, The Ciao Development Team. *An Overview of The Ciao Multiparadigm Language and Program Development Environment and its Design Philosophy*. ECOOP Workshop on Multiparadigm Programming with Object-Oriented Languages MPOOL 2007, July 2007.
18. M. Méndez-Lojo, J. Navas, M. Hermenegildo. *An Efficient, Parametric Fixpoint Algorithm for Analysis of Java Bytecode*. ETAPS Workshop on Bytecode Semantics, Verification, Analysis and Transformation (BYTECODE 2007), Electronic Notes in Theoretical Computer Science, Elsevier - North Holland, March 2007.
19. E. Albert, P. Arenas, G. Puebla, M. Hermenegildo. *Generation of Reduced Certificates in Abstraction-Carrying Code*. VI Jornadas Programación y Lenguajes (PROLE'06), 10 pages, October 2006.
20. E. Mera, P. López-García, G. Puebla, M. Carro, M. Hermenegildo. *Towards Execution Time Estimation for Logic Programs via Static Analysis and Profiling*. 16th Workshop on Logic Programming Environments, pages 45–60, University of Namur, Institut d'Informatique, August 2006.
21. J. Navas, F. Bueno, M. Hermenegildo. *A study of set-sharing analysis via cliques*. The 15th Workshop on Logic-Based Methods in Programming Environments, WLPE'05, 15 pages, October 2005.
22. A. Casas, D. Cabeza, M. Hermenegildo. *Functional Notation and Lazy Evaluation in Ciao*. Colloquium on Implementation of Constraint and LOGic Programming Systems (CICLOPS'05, ICLP associated workshop), pages 25–36, October 2005.
23. G. Puebla, E. Albert, M. Hermenegildo. *A Generic Framework for the Analysis and Specialization of Logic Programs*. The 15th Workshop on Logic-Based Methods in Programming Environments, WLPE'05, 15 pages, October 2005.

24. E. Albert, G. Puebla, M. Hermenegildo. *Abstract Interpretation-based Code Certification for Pervasive Systems: Preliminary Experiments*. Workshop on Software Analysis and Development for Pervasive Systems (SONDA'04), pages 1–6, August 2004.
25. E. Albert, G. Puebla, M. Hermenegildo. *An Abstract Interpretation-based Approach to Mobile Code Safety*. Proc. of Compiler Optimization meets Compiler Verification (COCV'04), Electronic Notes in Theoretical Computer Science 132(1), pages 113–129, Elsevier - North Holland, April 2004.
26. J. Morales, M. Carro, M. Hermenegildo. *Improving the Compilation of Prolog to C Using Type and Determinism Information: Preliminary Results*. Colloquium on Implementation of Constraint and Logic Programming Systems (ICLP associated workshop), pages 89–102, December 2003.
27. J. Correas, J. M. Gomez, M. Carro, D. Cabeza, M. Hermenegildo. *A Generic Persistence Model for CLP Systems (And Two Useful Implementations)*. Second CoLogNet Workshop on Implementation Technology for Computational Logic Systems (Formal Methods '03 Workshop), pages 51–64, Facultad de Informatica, September 2003.
28. M. Carro, M. Hermenegildo. *A Simple Approach to Distributed Objects in Prolog*. Colloquium on Implementation of Constraint and Logic Programming Systems (ICLP associated workshop), July 2002.
29. S. Muñoz, J.J. Moreno-Navarro, M. Hermenegildo. *Efficient Implementation of General Negation Using Abstract Interpretation*. Proc. of the AGP'2000 Joint Conference on Declarative Programming, 15 pages, Italian Association for Logic Programming, December 2000.
30. F. Bueno, M. Garcia de la Banda, M. Hermenegildo, K. Marriott, G. Puebla, P. Stuckey. *A Model for Inter-module Analysis and Optimizing Compilation*. Tenth International Workshop on Logic-based Program Synthesis and Transformation, July 2000.
31. G. Puebla, M. Hermenegildo. *Some Issues in Analysis and Specialization of Modular Ciao-Prolog Programs*. Special Issue on Optimization and Implementation of Declarative Programming Languages, Electronic Notes in Theoretical Computer Science, Vol. 30, Num. 2, 25 pages, Elsevier - North Holland, March 2000.
32. D. Cabeza, M. Hermenegildo. *The Ciao Module System: A New Module System for Prolog*. Special Issue on Parallelism and Implementation of (C)LP Systems, Electronic Notes in Theoretical Computer Science, Vol. 30, Num. 3, Elsevier - North Holland, March 2000.
33. D. Cabeza, M. Hermenegildo. *The Ciao Modular, Standalone Compiler and Its Generic Program Processing Library*. Special Issue on Parallelism and Implementation of (C)LP Systems, Electronic Notes in Theoretical Computer Science, Vol. 30(3), Elsevier - North Holland, March 2000.
34. M. Hermenegildo. *A System for Automatically Generating Documentation for (C)LP Programs*. Special Issue on Parallelism and Implementation of (C)LP Systems, Electronic Notes in Theoretical Computer Science, Vol. 30, Num. 4, March 2000.
35. D. Cabeza, M. Hermenegildo. *A New Module System for Prolog*. ICLP'99 WS on Parallelism and Implementation of (C)LP Systems, pages 110–128, N.M. State U., December 1999.
36. D. Cabeza, M. Hermenegildo. *The Ciao Modular Compiler and Its Generic Program Processing Library*. ICLP'99 WS on Parallelism and Implementation of (C)LP Systems, pages 147–164, N.M. State U., December 1999.
37. M. Hermenegildo. *A Documentation Generator for Logic Programming Systems*. ICLP'99 Workshop on Logic Programming Environments, pages 80–97, N.M. State University, December 1999.

38. G. Puebla, M. Hermenegildo, J. Gallagher. *An Integration of Partial Evaluation in a Generic Abstract Interpretation Framework*. ACM SIGPLAN Workshop on Partial Evaluation and Semantics-Based Program Manipulation (PEPM'99), BRISC Series, Num. NS-99-1, pages 75–85, University of Aarhus, Denmark, January 1999.
39. M. Carro, M. Hermenegildo. *Some Design Issues in the Visualization of Constraint Program Execution*. AGP'98 Joint Conference on Declarative Programming, pages 71–86, July 1998.
40. G. Puebla, F. Bueno, M. Hermenegildo. *A Framework for Assertion-based Debugging in Constraint Logic Programming*. Proceedings of the JICSLP'98 Workshop on Types for CLP, pages 3–15, June 1998.
41. G. Puebla, F. Bueno, M. Hermenegildo. *An Assertion Language for Debugging of Constraint Logic Programs*. Proceedings of the ILPS'97 Workshop on Tools and Environments for (Constraint) Logic Programming, October 1997. Available from ftp://clip.dia.fi.upm.es/pub/papers/assert_lang_t as technical report CLIP2/97.1..
42. G. Puebla, J. Gallagher, M. Hermenegildo. *Towards Integrating Partial Evaluation in a Specialization Framework based on Generic Abstract Interpretation*. Proceedings of the ILPS'97 Workshop on Specialization of Declarative Programs, October 1997. Post ILPS'97 Workshop.
43. D. Cabeza, M. Hermenegildo. *WWW Programming using Computational Logic Systems (and the PiLLoW/Ciao Library)*. Proceedings of the Workshop on Logic Programming and the WWW at WWW6, April 1997.
44. G. Puebla, M. García de la Banda, M. Hermenegildo, K. Marriott, P. Stuckey. *Automatic Optimization of Logic Programs with Dynamic Scheduling*. Workshop on Abstract Interpretation of Logic Languages, The Hebrew University, December 1996.
45. M. Hermenegildo, F. Bueno, D. Cabeza, M. Carro, M. García de la Banda, P. López-García, G. Puebla. *The CIAO Multi-Dialect Compiler and System: A Demo and Status Report*. Proceedings of the JICSLP'96 Workshop on Parallelism and Implementation Technology, 12 pages, Computer Science Department, Technical University of Madrid, September 1996. Available from <http://www.cliplab.org/Projects/COMPULOG/meeting96/papers/PS/clip.ps.gz>.
46. G. Puebla, M. Hermenegildo. *Optimized Algorithms for the Incremental Analysis of Logic Programs*. II Workshop on Verification and Analysis of Logic Languages, September 1996.
47. D. Cabeza, M. Hermenegildo, S. Varma. *The PiLLoW/Ciao Library for INTERNET/WWW Programming using Computational Logic Systems*. Proceedings of the 1st Workshop on Logic Programming Tools for INTERNET Applications, pages 72–90, September 1996. Text and code available from <http://www.cliplab.org/miscdocs/pillow/pillow.html>.
48. D. Cabeza, M. Hermenegildo. *Implementing Distributed Concurrent Constraint Execution in the CIAO System*. Proc. of the AGP'96 Joint conference on Declarative Programming, pages 67–78, U. of the Basque Country, July 1996. Available from <http://www.cliplab.org/>.
49. M. Hermenegildo, F. Bueno, D. Cabeza, M. Carro, M. García de la Banda, P. López, G. Puebla. *The CIAO Multi-Dialect Compiler and System: An Experimentation Workbench for Future (C)LP Systems*. Proc. of the AGP'96 Joint Conference on Declarative Programming, pages 105–108, U. of the Basque Country, July 1996. Available from <ftp://www.cliplab.org/pub/papers>.
50. G. Puebla, M. Hermenegildo. *Abstract Specialization and its Application to Program Parallelization*. V International Workshop on Metaprogramming and Metareasoning in Logic, 1996.
51. M. Hermenegildo, F. Bueno, M. García de la Banda, G. Puebla. *The CIAO Multi-Dialect Compiler and System: An Experimentation Workbench for Future (C)LP Systems*. Proceedings of the ILPS'95 Workshop on Visions for the Future of Logic Programming, December 1995. Available from <http://www.cliplab.org/>.

52. M. García de la Banda, F. Bueno, M. Hermenegildo. *Towards Automatic Compile-Time Parallelization of CLP Programs by Analysis and Transformation*. Proceedings of the ILPS'95 Workshop on Parallel Logic Programming Systems, U. of Oregon, December 1995.
53. D. Cabeza, M. Hermenegildo. *Distributed Concurrent Constraint Execution in the CIAO System*. Proc. of the 1995 COMPULOG-NET Workshop on Parallelism and Implementation Technologies, U. Utrecht / T.U. Madrid, September 1995. Available from <http://www.cliplab.org/>.
54. M. Hermenegildo, The CLIP Group. *The CIAO Multiparadigm Compiler and System: A Progress Report*. Proc. of the Compulog Net Area Workshop on Parallelism and Implementation Technologies, Technical University of Madrid, September 1995.
55. F. Bueno, D. Cabeza, M. Hermenegildo, G. Puebla. *Data-flow Analysis of Standard Prolog Programs*. ICLP95 WS on Abstract Interpretation of Logic Languages, June 1995.
56. F. Bueno, M. Hermenegildo. *Analysis of Concurrent Constraint Logic Programs with a Fixed Scheduling Rule*. ICLP95 WS on Abstract Interpretation of Logic Languages, June 1995.
57. M. Hermenegildo, D. Cabeza, M. Carro. *On The Uses of Attributed Variables in Parallel and Concurrent Logic Programming Systems*. Proc. of the 1994 COMPULOG-NET Workshop Parallelism and Implementation Technologies, 15 pages, U. of Madrid, September 1994. Also provided as attachment UPM-1 of deliverable D4.3/2.
58. M. Hermenegildo, M. Carro. *A Note on Data-Parallelism and (And-Parallel) Prolog*. ICLP WS on Parallel and Data Parallel Execution of Logic Programs, 16 pages, June 1994.
59. M. Fernández, M. Carro, M. Hermenegildo. *IDRA (Ideal Resource Allocation): A Tool for Computing Ideal Speedups*. ICLP WS on Parallel and Data Parallel Execution of Logic Programs, 16 pages, June 1994.
60. M. Hermenegildo, The CLIP Group. *Some Methodological Issues in the Design of CIAO - A Generic, Parallel Concurrent Constraint System*. Proc. of the 1994 ICOT/NSF Workshop on Parallel and Concurrent Programming, U. of Oregon, March 1994.
61. P. López-García, M. Hermenegildo. *Towards Dynamic Term Size Computation via Program Transformation*. Second Spanish Conference on Declarative Programming, pages 73–93, IIIA/CSIC, September 1993.
62. M. Hermenegildo, The CLIP Group. *Towards CIAO-Prolog – A Parallel Concurrent Constraint System*. Proc. of the Compulog Net Area Workshop on Parallelism and Implementation Technologies, 10 pages, FIM/UPM, Madrid, Spain, June 1993.
63. M. Carro, L. Gómez, M. Hermenegildo. *Implementation of an Event Driven Scheme for Visualizing Parallel Execution of Logic Programs*. JICSLP'92 Workshop on Parallel Execution, 15 pages, November 1992.
64. M. Carro, L. Gómez, M. Hermenegildo. *Implementation of an Event Driven Scheme for Visualizing Parallel Execution of Logic Programs*. Primer Congreso Nacional de Programación Declarativa, pages 262–278, FIM/UPM, September 1992.
65. G. Gupta, M. Hermenegildo, V. Santos-Costa. *Generalized Stack Copying for And-Or Parallel Implementations*. JICSLP'92 Workshop on Parallel Execution, 15 pages, 1992.
66. G. Gupta, M. Hermenegildo. *ACE: And/Or-parallel Copying-based Execution of Logic Programs*. ICLP '91 Workshop on Parallel Execution of Logic Programs, LNCS, Num. 569, pages 146–159, Springer-Verlag, December 1991.
67. M. Hermenegildo, M. Carro. *Experimenting with Independent And-Parallel Prolog using Standard Prolog*. Jornadas Nacionales de Programación Declarativa, pages 478–497, U. Malaga, October 1991.

68. M. García de la Banda, M. Hermenegildo. *Some Considerations on the Compile-Time Analysis of Constraint Logic Programs*. Jornadas Nacionales de Programación Declarativa, pages 97–117, U. Malaga, October 1991.
69. F. Bueno, M. Hermenegildo. *Towards an Automatic Translation Algorithm from Prolog to the Andorra Kernel Language*. Proc. of the 1991 GULP Conference on Logic Programming, pages 489–505, Italian Association for Logic Programming, June 1991.
70. M. Hermenegildo. *Compile-time Analysis Requirements for the Extended Andorra Model*. Parallel Logic Programming Workshop, SICS, June 1990.
71. I. Walker, M. Hermenegildo. *An application of AI techniques and parallel implementation technology to the trajectory planning problem for redundant robot manipulators*. 1987 IEEE International Workshop on Robotics: Trends, Technology, and Applications, pages 53–61, ETSII, IEEE, February 1988.

Technical Reports and Manuals

1. P. López-García, L. Darmawan, F. Bueno, M. Hermenegildo. *Towards Resource Usage Function Verification based on Input Data Size Intervals*. Num. CLIP4/2011.0, Technical University of Madrid (UPM), April 2011. Available at <http://cliplab.org/papers/resource-verif-11-tr.pdf>.
2. P. Chico de Guzmán, A. Casas, M. Carro, M. Hermenegildo. *A Simulation Study on Parallel Backtracking with Solution Memoing for Independent And-Parallelism*. Num. CLIP1/2011.0, 12 pages, Technical University of Madrid (UPM), January 2011.
3. J.F. Morales, M. Hermenegildo, R. Haemmerlé. *Towards Modular Extensions for a Modular Language*. Num. CLIP2/2011.0, 15 pages, Technical University of Madrid (UPM), January 2011.
4. D. Ivanović, M. Carro, M. Hermenegildo. *Automated Attribute Inference in Complex Service Workflows Based on Sharing Analysis*. Num. CLIP5/2010.0, Technical University of Madrid (UPM), December 2010.
5. D. Ivanović, M. Carro, M. Hermenegildo. *Automatic Fragment Identification in Workflows Based on Sharing Analysis*. Num. CLIP4/2010.0, 15 pages, Technical University of Madrid (UPM), June 2010. Submitted.
6. E. Mera, T. Trigo, P. López-García, M. Hermenegildo. *An Approach to Profiling for Run-Time Checking of Computational Properties and Performance Debugging*. Num. CLIP3/2010.0, 29 pages, Technical University of Madrid (UPM), March 2010.
7. M. V. Hermenegildo, F. Bueno, M. Carro, P. López, E. Mera, J.F. Morales, G. Puebla. *An Overview of Ciao and its Design Philosophy*. Num. CLIP2/2010.0, Technical University of Madrid (UPM), March 2010. Under consideration for publication in *Theory and Practice of Logic Programming (TPLP)*.
8. D. Ivanović, M. Carro, M. Hermenegildo, P. López, E. Mera. *Towards Data-Aware Cost-Driven Adaptation for Service Orchestrations*. Num. CLIP5/2009.1, Technical University of Madrid (UPM), March 2010.
9. P. López-García, L. Darmawan, F. Bueno, M. Hermenegildo. *Towards a Framework for Resource Usage Verification and Debugging in the CiaoPP System*. Num. CLIP1/2010.0, Technical University of Madrid (UPM), February 2010. Available at <http://cliplab.org/papers/resource-verif-10-tr.pdf>.
10. D. Ivanović, M. Carro, M. Hermenegildo. *Sharing-Based Independence-Driven Fragment Identification for Service Orchestrations*. Num. CLIP7/2009.0, Technical University of Madrid (UPM), December 2009.

11. D. Ivanović, M. Carro, M. Hermenegildo, P. López, E. Mera. *Towards Data-Aware Cost-Driven Adaptation for Service Orchestrations*. Num. CLIP5/2009.0, Technical University of Madrid (UPM), November 2009. Replaced by a later version.
12. J.F. Morales, M. Carro, M. Hermenegildo. *Description and Optimization of Abstract Machines in a Dialect of Prolog*. Num. CLIP4/2009.0, Technical University of Madrid (UPM), October 2009.
13. D. Ivanović, M. Carro, M. Hermenegildo. *Towards Data-Aware Resource Analysis for Service Orchestrations*. Num. CLIP3/2009.0, Technical University of Madrid (UPM), June 2009.
14. E. Mera, P. López-García, M. Hermenegildo. *Towards Integrating Run-Time Checking and Software Testing in a Verification Framework*. Num. CLIP1/2009.0, 19 pages, Technical University of Madrid (UPM), March 2009.
15. P. López-García, F. Bueno, M. Hermenegildo. *Inferring Determinacy and Mutual Exclusion in Logic Programs Using Mode and Type Analysis*. Num. CLIP2/2009.0, Technical University of Madrid (UPM), February 2009.
16. E. Trias, J. Navas, E. S. Ackley, S. Forrest, M. Hermenegildo. *Efficient Representations for Set-Sharing Analysis*. Num. CLIP9/2008.0, University of New Mexico and Technical University of Madrid, September 2008.
17. P. Chico de Guzmán, M. Carro, M. V. Hermenegildo. *Bridge Program Transformation for the CCall Tabling Scheme*. Num. CLIP6/2008.0, Technical University of Madrid (UPM), September 2008.
18. A. Casas, M. Carro, M. Hermenegildo. *A High-Level Implementation of Non-Deterministic, Unrestricted, Independent And-Parallelism*. Num. TR-CS-2008-10, University of New Mexico (UNM), September 2008.
19. J. Navas, E. Mera, P. López-García, M. Hermenegildo. *Inference of User-Definable Resource Bounds Usage for Logic Programs and its Applications*. Num. CLIP5/2008.0, Technical University of Madrid (UPM), July 2008.
20. M. Méndez-Lojo, O. Lhoták, M. Hermenegildo. *Fast Set Sharing using ZBDDs*. University of New Mexico, June 2008.
21. M. Hermenegildo, E. Albert, P. Arenas, F. Bueno, M. Carro, A. Casas, P. Chico de Guzmán, J. Correas, S. Genaim, J. Lipton, Pedro López García, M. Méndez, E. Mera, J. Morales, J. Navas, R. Padilla, P. Pietrzak, G. Puebla, M. Zamalloa, D. Zanardini. *Rigorous Methods for Mobile and Heterogeneous Software Systems – Second Year Report*. Num. CLIP3/2008.0, Technical University of Madrid (UPM), April 2008.
22. J. Navas, M. Méndez-Lojo, M. Hermenegildo. *Customizable Resource Usage Analysis for Java Bytecode*. Num. UNM TR-CS-2008-02 - CLIP1/2008.0, University of New Mexico, January 2008.
23. A. Casas, M. Carro, M. Hermenegildo. *Automatic Unrestricted Independent And-Parallelism in Logic Programs*. Num. CLIP11/2007.0, Technical University of Madrid (UPM), December 2007. Under consideration for publication in *Theory and Practice of Logic Programming (TPLP)*.
24. M. Méndez-Lojo, M. Hermenegildo. *Precise Set Sharing for Java-style Programs (and proofs)*. Num. CLIP2/2007.1, Technical University of Madrid (UPM), November 2007.
25. A. Casas, M. Carro, M. Hermenegildo. *Towards a High-Level Implementation of Execution Primitives for Non-restricted, Independent And-parallelism*. Num. TR-CS-2007-16, University of New Mexico (UNM), October 2007.
26. A. Casas, M. Carro, M. Hermenegildo. *Annotation Algorithms for Unrestricted Independent And-Parallelism in Logic Programs*. Num. TR-CS-2007-14, University of New Mexico (UNM), September 2007.

27. E. Mera, P. López-García, M. Carro, M. Hermenegildo. *Towards Execution Time Estimation in Abstract Machine-Based (Logic) Languages*. Num. CLIP8/2007.0, Technical University of Madrid (UPM), August 2007.
28. P. Chico de Guzmán, M. Carro, M. V. Hermenegildo. *An Improved Continuation Call-Based Implementation of Tabling*. Num. CLIP9/2007.0, Technical University of Madrid (UPM), August 2007.
29. A. Casas, M. Carro, M. V. Hermenegildo. *Towards a High-Level Implementation of Execution Primitives for Non-Restricted, Independent And-Parallelism*. Num. CLIP7/2007.0, Technical University of Madrid (UPM), August 2007.
30. A. Casas, M. Carro, M. Hermenegildo. *Annotation Algorithms for Unrestricted Independent And-Parallelism in Logic Programs*. Num. CLIP5/2007.0, Technical University of Madrid (UPM), June 2007.
31. M. Hermenegildo, E. Albert, P. Arenas, A. Beascoa, F. Bueno, D. Cabeza, M. Carro, J. Correas, A. García Pañoso, J. Lipton, Pedro López García, E. Mera, J. Morales, C. Ochoa, G. Puebla. *Rigorous Methods for Mobile and Heterogeneous Software Systems – First Year Report*. Num. CLIP4/2007.0, Technical University of Madrid (UPM), March 2007.
32. M. Méndez-Lojo, M. Hermenegildo. *Precise Set Sharing and Nullity Analysis for Java-style Programs*. Num. CLIP2/2007.0, Technical University of Madrid (UPM), February 2007.
33. P. López-García, F. Bueno, M. Hermenegildo. *Inferring Determinacy in Logic Programs Using Mode and Type Information*. Num. CLIP3/2007.0, Technical University of Madrid (UPM), February 2007.
34. M. Méndez-Lojo, J. Navas, M. Hermenegildo. *Efficient, Parametric Analysis of Java Bytecode*. Num. CLIP9/2006.0, Technical University of Madrid (UPM), December 2006.
35. J. Morales, M. Carro, M. Hermenegildo. *Description and Optimization of Abstract Machines in an Extension of Prolog*. Num. CLIP8/2006.0, 43 pages, Technical University of Madrid (UPM), December 2006.
36. M. Hermenegildo, The Ciao Development Team. *Why Ciao? –An Overview of the Ciao System's Design Philosophy*. Num. CLIP7/2006.0, 6 pages, Technical University of Madrid (UPM), December 2006. Available from: <http://cliplab.org/papers/ciao-philosophy-note-tr.pdf>.
37. J. Navas, E. Mera, P. López-García, M. Hermenegildo. *Resource Bounds Analysis*. Num. CLIP6/2006.0, 19 pages, Technical University of Madrid (UPM), November 2006.
38. P. Pietrzak, M. Hermenegildo. *Improved Automatic Error Diagnosis Based on Abstract Interpretation*. Num. CLIP11/2006.0, Technical University of Madrid, September 2006.
39. E. Mera, P. López-García, G. Puebla, M. Carro, M. Hermenegildo. *Towards Combining Static Analysis and Profiling for Estimating Execution Times in Logic Programs*. Num. CLIP5/2006.0, 15 pages, Technical University of Madrid (UPM), April 2006.
40. M. Carro, J. Morales, H.L. Muller, G. Puebla, M. Hermenegildo. *Optimizing Prolog for Small Devices: A Case Study*. Num. CLIP4/2006.0, 16 pages, Technical University of Madrid, April 2006. Under consideration for publication.
41. F. Bueno, P. López-García, G. Puebla, M. Hermenegildo. *The Ciao Preprocessor*. Num. CLIP1/06, 99 pages, Technical University of Madrid (UPM), January 2006.
42. F. Bueno, P. López-García, G. Puebla, M. Hermenegildo. *A Tutorial on Program Development and Optimization using the Ciao Preprocessor*. Num. CLIP2/06, 55 pages, Technical University of Madrid (UPM), January 2006.

43. A. Casas, D. Cabeza, M. Hermenegildo. *A Syntactic Approach to Combining Functional Notation, Lazy Evaluation and Higher-Order in LP Systems*. Num. TR-CS-2006-07, 17 pages, University of New Mexico (UNM), 2006.
44. E. Albert, P. Arenas, G. Puebla, M. Hermenegildo. *Reduced Certificates for Abstraction-Carrying Code*. Num. CLIP8/2005.0, 19 pages, Technical University of Madrid (UPM), October 2005.
45. E. Albert, G. Puebla, M. Hermenegildo. *Abstraction-Carrying Code: A Model for Mobile Code Safety*. Num. CLIP7/2005.0, 30 pages, Technical University of Madrid, July 2005.
46. G. Puebla, E. Albert, M. Hermenegildo. *Abstract Interpretation with Specialized Definitions*. Num. CLIP6/2005.0, Technical University of Madrid, July 2005.
47. J. Morales, M. Carro, G. Puebla, M. Hermenegildo. *A Generator of Efficient Abstract Machine Implementations and its Application to Emulator Minimization*. Num. CLIP3/2005.0, 21 pages, Technical University of Madrid (UPM), May 2005.
48. F. Bueno, J. Navas, M. Hermenegildo. *Sharing, Freeness, Linearity, Redundancy, Widenings, and Cliques*. Num. CLIP5/2005.0, 21 pages, Technical University of Madrid (UPM), April 2005.
49. G. Puebla, E. Albert, M. Hermenegildo. *Efficient Local Unfolding with Ancestor Stacks for Full Prolog*. Num. CLIP2/2005.0, Technical University of Madrid, February 2005.
50. F. Bueno, D. Cabeza, M. Carro, M. Hermenegildo, P. López-García, G. Puebla-(Eds.). *The Ciao System. Reference Manual (V1.10)*. School of Computer Science, Technical University of Madrid (UPM), June 2004. System and on-line version of the manual available at <http://www.ciaohome.org>.
51. M. Carro, J. M. Gomez, J. Correas, J. F. Morales, E. Mera, G. Puebla, D. Cabeza, F. Bueno, C. Daffara, M. Hermenegildo. *Web site*. Num. CLIP9/2004.0, 2 pages, School of Computer Science, Technical University of Madrid, May 2004. Deliverable D17 of the AMOS Project.
52. M. Carro, J. Correas, C. Daffara, G. Puebla, M. Hermenegildo. *Information Spreading*. Num. CLIP10/2004.0, 273 pages, School of Computer Science, Technical University of Madrid, May 2004. Deliverable D18 of the AMOS Project.
53. J. Morales, M. Carro, M. Hermenegildo. *Improving the Compilation of Prolog to C Using Moded Types and Determinism Information*. Num. CLIP5/2004.0, Technical University of Madrid, April 2004.
54. J. Correas, J. M. Gomez, M. Carro, D. Cabeza, M. Hermenegildo. *A Generic Persistence Model for (C)LP Systems (and two useful implementations)*. Num. CLIP3/2003.1(2004), 16 pages, Technical University of Madrid, April 2004. <http://cliplab.org/papers/persdb-tr1.pdf>.
55. M. Carro, J. M. Gomez, J. Correas, J. F. Morales, E. Mera, G. Puebla, D. Cabeza, F. Bueno, C. Daffara, M. Hermenegildo. *AMOS User's Manual*. Num. CLIP4/2004.0, 44 pages, Technical University of Madrid, March 2004.
56. F. Bueno, P. López-García, G. Puebla, M. Hermenegildo. *The Ciao Prolog Preprocessor*. Num. CLIP1/04, 72 pages, Technical University of Madrid (UPM), January 2004.
57. J. Navas, D. Cabeza, M. Hermenegildo. *CoLogNetWS: Development of a Web Site with Database Multinode Distributed Update*. Num. CLIP6/2003.0, 157 pages, CoLogNet Project, November 2003.
58. E. Albert, G. Puebla, M. Hermenegildo. *An Abstract Interpretation-based Approach to Mobile Code Safety*. Num. CLIP8/2003.0, 18 pages, Technical University of Madrid, November 2003.
59. G. Puebla, J. Correas, M. Hermenegildo. *The Design of a Generic Framework for Context-Sensitive Analysis of Modular Programs*. Num. CLIP7/2003.0, Technical University of Madrid, November 2003.

60. J. Correas, J. M. Gomez, M. Carro, D. Cabeza, M. Hermenegildo. *A Generic Model for Persistence in CLP Systems*. Num. CLIP3/2003.0, 17 pages, Technical University of Madrid, August 2003. <http://cliplab.org/papers/persdb-tr.pdf>.
61. F. Bueno, D. Cabeza, M. Carro, M. Hermenegildo, P. López-García, G. Puebla. *The Ciao Prolog System. Reference Manual (V1.8)*. Num. CLIP4/2002.1, School of Computer Science, Technical University of Madrid (UPM), May 2002. System and on-line version of the manual available at <http://www.ciaohome.org>.
62. M. Carro, M. Hermenegildo. *Remote Execution and Mobile Objects in Ciao Prolog*. Num. CLIP3/2001.1, Facultad de Informática, UPM, Technical University of Madrid (UPM), 2001.
63. F. Bueno, M. Hermenegildo, G. Puebla, P. J. Stuckey. *Safety for Logic Programs*. Num. CLIP1/2001.1, Facultad de Informática, UPM, Technical University of Madrid (UPM), 2001.
64. F. Bueno, D. Cabeza, M. Carro, M. Hermenegildo, P. López-García, G. Puebla. *The Ciao Prolog System. Reference Manual (V1.6)*. Num. CLIP5/2000.1, School of Computer Science, Technical University of Madrid (UPM), July 2000. System and on-line version of the manual available at <http://www.ciaohome.org>.
65. M. Hermenegildo, D. Cabeza, M. Rodriguez. *Using Ciao inside GNU Emacs*. Num. CLIP4/2000.1, 18 pages, Facultad de Informática, UPM, Technical University of Madrid (UPM), 2000.
66. D. Cabeza, M. Hermenegildo. *Higher-order Logic Programming in Ciao*. Num. CLIP7/99.0, 15 pages, Facultad de Informática, UPM, Technical University of Madrid (UPM), September 1999.
67. D. Cabeza, M. Hermenegildo. *A New Module System for Prolog*. Num. CLIP8/99.0, 15 pages, Facultad de Informática, UPM, Technical University of Madrid (UPM), September 1999.
68. D. Cabeza, M. Hermenegildo. *The Ciao Modular Compiler and Its Generic Program Processing Library*. Num. CLIP9/99.0, 15 pages, Facultad de Informática, UPM, Technical University of Madrid (UPM), September 1999.
69. M. Hermenegildo. *A Documentation Generator for Logic Programming Systems*. Num. CLIP10/99.0, 15 pages, Facultad de Informática, UPM, Technical University of Madrid (UPM), September 1999.
70. A. Pineda, M. Hermenegildo. *O'Ciao: An Object Oriented Programming Model for (CIAO) Prolog*. Num. CLIP 5/99.0, Facultad de Informática, UPM, July 1999.
71. F. Bueno, D. Cabeza, M. Carro, M. Hermenegildo, P. López-García, G. Puebla. *The Ciao Prolog System. Reference Manual (V1.0)*. Num. CLIP11/99.1, School of Computer Science, Technical University of Madrid (UPM), June 1999. System and on-line version of the manual available at <http://www.ciaohome.org>.
72. F. Bueno, P. López-García, G. Puebla, M. Hermenegildo, P. Pietrzak. *The CHIP Assertion Pre-processor*. Num. CLIP1/99.1, 50 pages, Technical University of Madrid (UPM), March 1999. Also as deliverable of the ESPRIT project DISCIPL.
73. M. Carro, M. Hermenegildo. *Some Issues in the Design of Visualizations for Constraint Logic Program Execution*. Num. CLIP2/99.1, 17 pages, Technical University of Madrid (UPM), March 1999. Also as deliverable of the ESPRIT project DISCIPL.
74. J.M. Gomez, D. Cabeza, M. Hermenegildo. *persdb: Persistent Database Interface*. Num. CLIP9/98.0, 24 pages, December 1998.
75. I. Caballero, D. Cabeza, S. Genaim, J.M. Gomez, M. Hermenegildo. *persdb_sql: SQL Persistent Database Interface*. Num. CLIP10/98.0, 39 pages, December 1998.
76. J.M. Gomez, D. Cabeza, M. Hermenegildo. *WebDB: A Database WWW Interface*. Num. CLIP11/98.0, 32 pages, December 1998.

77. M. Hermenegildo, G. Puebla, F. Bueno. *Using Global Analysis, Partial Specifications, and an Extensible Assertion Language for Program Validation and Debugging*. Num. CLIP8/98.0, Technical University of Madrid (UPM), October 1998.
78. D. Cabeza, M. Hermenegildo, S. Genaim, C. Taboch. *Design of a Generic, Homogeneous Interface to Relational Databases*. Num. D3.1.M1-A1, CLIP7/98.0, 10 pages, September 1998.
79. F. Bueno, D. Cabeza, M. Carro, M. Hermenegildo, P. López-García, G. Puebla. *The Ciao Prolog System. Reference Manual (V0.6)*. Num. CLIP12/98.1, School of Computer Science, Technical University of Madrid (UPM), July 1998. System and on-line version of the manual available at <http://www.ciaohome.org>.
80. D. Cabeza, M. Hermenegildo. *A WWW Database Management Interface for Prolog*. Num. CLIP2/98.0, School of Computer Science, Technical University of Madrid (UPM), June 1998.
81. M. Carro, M. Hermenegildo, F. Bueno, D. Cabeza, M. J. García, P. López-García. *An Introductory Course on Constraint Logic Programming*. 130 pages, Technical University of Madrid, 1998.
82. M. Carro, M. Hermenegildo. *Some Design Issues in Constraint Program Visualization and Abstraction*. Num. CLIP1/97.1, 20 pages, Technical University of Madrid (UPM), September 1997. Also as deliverable of the ESPRIT project DISCIPL.
83. F. Bueno, D. Cabeza, M. Carro, M. Hermenegildo, P. López-García, G. Puebla. *The Ciao Prolog System. Reference Manual*. Num. CLIP3/97.1, School of Computer Science, Technical University of Madrid (UPM), August 1997. System and on-line version of the manual available at <http://www.ciaohome.org>.
84. M. Hermenegildo, The CLIP Group. *An Automatic Documentation Generator for (C)LP – Reference Manual*. Num. CLIP5/97.3, Facultad de Informática, UPM, August 1997. Online at <http://www.ciaohome.org>.
85. A. Aggoun, F. Benhamou, F. Bueno, M. Carro, P. Deransart, W. Drabent, G. Ferrand, F. Goualard, M. Hermenegildo, C. Lai, J.Lloyd, J. Maluszynski, G. Puebla, A. Tessier. *CP Debugging Tools: Clarification of Functionalities and Selection of the Tools*. Num. D.WP1.1.M1.1-2, 15 pages, DISCIPL Project, June 1997.
86. S.K. Debray, P. López-García, M. Hermenegildo. *Towards Precise Non-Failure Analysis for Logic Programs*. Num. CLIP18/96.0, T.U. of Madrid (UPM), November 1996.
87. D. Cabeza, M. Hermenegildo. *html.pl: An HTML Package for (C)LP systems*. Technical University of Madrid, March 1996. Available from <http://www.cliplab.org/miscdocs/>.
88. G. Puebla, M. Hermenegildo. *Optimized Algorithms for Incremental Analysis of Logic Programs*. Num. CLIP3/96.0, Facultad de Informática, UPM, February 1996.
89. G. Puebla, M. Hermenegildo. *Automatic Optimization of Dynamic Scheduling in Logic Programs*. Technical University of Madrid, January 1996.
90. S.K. Debray, P. López-García, M. Hermenegildo, N.-W. Lin. *Lower Bound Cost Estimation for Logic Programs*. Num. CLIP20/95.0, T.U. of Madrid (UPM), December 1995.
91. M. Hermenegildo. *A WWW Interface Library for LP and CLP Systems*. Num. CLIP16/95.0, School of Computer Science, Technical University of Madrid (UPM), December 1995.
92. G. Puebla, M. Hermenegildo. *Specialization and Optimization of Constraint Programs with Dynamic Scheduling*. Num. CLIP12/95.0, Facultad de Informática, UPM, September 1995. Presented at the 1995 COMPULOG Meeting on Program Development.
93. F. Bueno, M. Hermenegildo. *Compiling Concurrency into a Sequential Logic Language*. Facultad de Informática, UPM, June 1995.

94. M. Hermenegildo, M. Carro. *A Note on Data-Parallelism and (And-Parallel) Prolog*. Num. CLIP 6/94.0, 15 pages, School of Computer Science, Technical University of Madrid (UPM), June 1995.
95. E. Pontelli, G. Gupta, D. Tang, M. Hermenegildo, M. Carro. *Efficient Implementation of And-parallel Prolog Systems*. Num. CLIP4/95.0, T.U. of Madrid (UPM), June 1995.
96. F. Bueno, M. Hermenegildo. *An Automatic Translation Scheme from CLP to AKL*. Facultad de Informática, UPM, June 1995.
97. M. García de la Banda, F. Bueno, M. Hermenegildo. *Automatic Compile-Time Parallelization of CLP Programs by Analysis and Transformation to a Concurrent Constraint Language*. Facultad de Informática, UPM, June 1995. Also in ILPS'95 WS on Parallel Logic Programming Systems.
98. F. Bueno, S. K. Debray, M. García de la Banda, M. Hermenegildo. *Transformation-based Implementation and Optimization of Programs Exploiting the Basic Andorra Model*. Num. CLIP11/95.0, Facultad de Informática, UPM, May 1995.
99. F. Bueno, D. Cabeza, M. Hermenegildo, G. Puebla. *Data-Flow Analysis of Prolog Programs with Extra-Logical Features*. Num. CLIP2/95.0, Computer Science Dept., Technical U. of Madrid (UPM), March 1995.
100. M. García de la Banda, M. Hermenegildo, K. Marriott. *Independence and Search Space Preservation in Dynamically Scheduled Constraint Logic Languages*. Facultad de Informática, UPM, February 1995.
101. F. Bueno, D. Cabeza, M. García de la Banda, M. Hermenegildo, G. Puebla. *Abstract Functions for the Analysis of Builtins in the PLAI System*. Num. CLIP1/95.0, Computer Science Dept., Technical U. of Madrid (UPM), January 1995.
102. M. Hermenegildo, K. Marriott, G. Puebla, P. Stuckey. *Incremental Analysis of Logic Programs*. Num. CLIP 14/94.0, Computer Science Dept., Technical U. of Madrid (UPM), October 1994.
103. S.K. Debray, P. López-García, M. Hermenegildo. *Non-Failure Analysis for Logic Programs*. Num. CLIP14/94.0, T.U. of Madrid (UPM), October 1994.
104. M. García de la Banda, M. Hermenegildo, K. Marriott. *Independence in Dynamically Scheduled Languages*. Num. CLIP12/94.0, T. University of Madrid (UPM), September 1994. Presented at the ICLP'93 Post Conference Workshop on Concurrent Constraint Logic Programming.
105. F. Bueno, S. K. Debray, M. García de la Banda, M. Hermenegildo. *QE-Andorra: A Quiche-Eating Implementation of the Basic Andorra Model*. Num. CLIP13/94.0, T.U. of Madrid (UPM), September 1994.
106. F. Bueno, F. Rossi, M. Hermenegildo. *Concurrent Semantics Based Optimizations of cc and CLP*. Num. CLIP2/93.2, T.U. of Madrid (UPM), September 1994.
107. M. García de la Banda, M. Hermenegildo, K. Marriott. *Search Space Preservation in CLP Languages*. Num. CLIP11/94.0, T. University of Madrid (UPM), September 1994.
108. F. Bueno, D. Cabeza, M. Hermenegildo, S. Prestwich, G. Puebla. *Some Improvements to an Abstract Syntax for Analysis and Optimization of Full (Parallel) Prolog*. Num. CLIP 8/94.0, Computer Science Dept., Technical U. of Madrid (UPM), September 1994.
109. G. Puebla, M. Hermenegildo. *An Implementation Technique for Multiple Program Specialization*. Num. CLIP7/94.0, Computer Science Dept., Technical U. of Madrid (UPM), August 1994.
110. M. Hermenegildo, D. Cabeza, M. Carro. *On The Uses of Attributed Variables in Parallel and Concurrent Logic Programming Systems*. Num. CLIP 5/94.0, 17 pages, School of Computer Science, Technical University of Madrid (UPM), June 1994. Presented at the ILPS'94 Post Conference Workshop on Design and Implementation of Parallel Logic Programming Systems, Available from <http://www.cliplab.org/>.

111. K. Shen, M. Hermenegildo. *A Flexible Scheduling and Memory Management Scheme for Non-Deterministic, And-parallel Execution*. Num. CLIP6/94.0, T.U. of Madrid (UPM), June 1994.
112. M. Hermenegildo. *A Simple, Distributed Version of the &-Prolog System*. School of Computer Science, Technical University of Madrid (UPM), April 1994. Available from <http://www.cliplab.org/>.
113. M. Hermenegildo, P. López-García. *A Technique for Dynamic Term Size Computation via Program Transformation*. Num. CLIP 8/93.1(94), School of Computer Science, Technical University of Madrid, March 1994.
114. S.K. Debray, P. López-García, M. Hermenegildo, N.-W. Lin. *Lower Bound Cost Estimation for Logic Programs*. Num. CLIP4/94.0, T.U. of Madrid (UPM), March 1994.
115. F. Bueno, M. García de la Banda, M. Hermenegildo. *The PLAI Abstract Interpretation System*. Num. CLIP2/94.0, Computer Science Dept., Technical U. of Madrid (UPM), February 1994.
116. F. Bueno, M. García de la Banda, M. Hermenegildo, U. Montanari, F. Rossi. *From Eventual to Atomic and Locally Atomic CC Programs: A Concurrent Semantics*. Num. CLIP1/94.0, T.U. of Madrid (UPM), January 1994.
117. F. Bueno, M. García de la Banda, M. Hermenegildo. *The MEL, UDG, and CDG Methods for Automatic Compile-time Parallelization of Logic Programs for Independent And-Parallelism: A Comparative Study*. Num. CLIP3/94.0, T.U. of Madrid (UPM), January 1994.
118. M. García de la Banda, M. Hermenegildo, M. Bruynooghe, V. Dumortier, G. Janssens, W. Simoens. *Global Analysis of Constraint Logic Programs*. U. of Madrid (UPM), 1994.
119. M. García de la Banda, M. Hermenegildo, K. Marriott. *Independence in Logic Programs with Dynamic Scheduling*. U. of Madrid (UPM), 1994.
120. F. Bueno, M. García de la Banda, M. Hermenegildo. *Effectiveness of Global Analysis in Strict Independence-Based Automatic Program Parallelization*. Num. CLIP7/93.0, T.U. of Madrid (UPM), October 1993.
121. D. Cabeza, M. Hermenegildo. *Towards Extracting Non-Strict Independent And-Parallelism Using Sharing and Freeness Information*. Num. CLIP5/92.1, U. of Madrid (UPM), August 1993.
122. F. Bueno, M. García de la Banda, M. Hermenegildo. *Compile-time Optimizations and Analysis Requirements for CC Programs*. Num. CLIP6/93.0, T.U. of Madrid (UPM), July 1993.
123. F. Bueno, M. García de la Banda, M. Hermenegildo, F. Rossi, U. Montanari. *Towards True Concurrency Semantics Based Program Transformation between CLP and CC*. Num. CLIP2/93.1(94), T.U. of Madrid (UPM), July 1993.
124. F. Bueno, M. García de la Banda, D. Cabeza, M. Hermenegildo. *The &-Prolog Compiler System — Automatic Parallelization Tools for LP*. Num. CLIP5/93.0, Computer Science Dept., Technical U. of Madrid (UPM), June 1993.
125. F. Bueno, M. Carro, D. Cabeza, F. Ballesteros, P. López-García, M. García de la Banda, M. Hermenegildo, L. Gómez, S. Prestwich, S.-W. Yan. *A Proposal for an Interchange Abstract Syntax for (Parallel) Prolog*. Num. CLIP3/93.0, 33 pages, Computer Science Dept., Technical U. of Madrid (UPM), June 1993.
126. P. López-García, M. Hermenegildo. *An Automatic Sequentializer Based on Program Transformation*. T.U. of Madrid (UPM), April 1993.
127. K. Shen, M. Hermenegildo. *A Flexible Scheduling and Memory Management Scheme for Non-Deterministic, And-parallel Execution of Logic Programs*. Num. CLIP 4/93.0, T.U. of Madrid (UPM), April 1993. Presented at the ICLP'93 Post Conference Workshop on Logic Program Implementation.

128. P. López-García, M. Hermenegildo. *Dynamic Term Size Computation in Logic Programs via Program Transformation*. Num. CLIP 8/93.0, T.U. of Madrid (UPM), April 1993. Presented at the 1993 COMPULOG Area Meeting on Parallelism and Implementation Technologies.
129. M. Carro, L. Gómez, M. Hermenegildo. *Using VisAndOr to Debug and Understand Parallel Execution of Logic Programs*. Num. CLIP 1/93.0, School of Computer Science, Technical University of Madrid, February 1993.
130. U. Montanari, F. Rossi, F. Bueno, M. García de la Banda, M. Hermenegildo. *Contextual Nets and Constraint Logic Programming: Towards a True Concurrent Semantics for CLP*. Num. CLIP 2/93.0, T.U. of Madrid (UPM), January 1993. Presented at the ICLP'93 Post Conference Workshop on Concurrent Constraint Logic Programming.
131. G. Gupta, M. Hermenegildo, E. Pontelli, V. Santos-Costa. *ACE: And/Or-parallel Copying-based Execution of Logic Programs*. New Mexico State University, Computer Science Dept., 1993.
132. G. Gupta, M. Hermenegildo, E. Pontelli, V. Santos-Costa. *ACE: And/Or-parallel Copying-based Execution of Logic Programs*. Num. CLIP7/92.0, U. of Madrid (UPM) and New Mexico S.U., November 1992.
133. M. García de la Banda, M. Hermenegildo, K. Marriott. *Independence in Constraint Logic Programs*. Num. CLIP6/92.0, U. of Madrid (UPM), November 1992.
134. K. Marriott, M. García de la Banda, M. Hermenegildo. *Analyzing Logic Programs with Dynamic Scheduling*. Num. CLIP6/93.1, T.U. of Madrid (UPM), October 1992.
135. M. Fernández, M. Carro, M. Hermenegildo. *IDRA (iDeal Resource Allocation): A Tool for Computing Ideal Speedups*. Num. FIM26.3/AI/92, 16 pages, School of Computer Science, Technical University of Madrid, September 1992. Presented at the ICLP'94 Post Conference Workshop on Parallel and Data Parallel Execution of Logic Programs.
136. D. Cabeza, M. Hermenegildo. *Extracting Non-Strict Independent And-Parallelism Using Sharing and Freeness Information*. Num. CLIP5/92.0, U. of Madrid (UPM), August 1992.
137. M. García de la Banda, M. Hermenegildo. *A Practical Approach to the Global Analysis of Constraint Logic Programs*. Num. without, U. of Madrid (UPM), April 1992. Presented at the 1992 FGCS Post-Conference Workshop on Constraint Logic Programming.
138. G. Janssens, M. Hermenegildo, F. Bueno, M. García de la Banda, A. Mulkers. *A Review of some Abstract Interpretation Systems*. Num. CW143, Department of Computer Science, Katholieke Universiteit Leuven, February 1992.
139. M. García de la Banda, M. Hermenegildo. *Effectiveness of Combined Sharing and Freeness Analysis using Abstract Interpretation*. Num. CLIP1/92.0, U. of Madrid (UPM), January 1992.
140. M. Hermenegildo, M. Carro. *Experimenting with Independent And-Parallel Prolog using Standard Prolog*. 24 pages, U. of Madrid (UPM), October 1991. Presented at the 1991 Spanish Workshop on Declarative Programming, Malaga.
141. M. Olmedilla, F. Bueno, M. Hermenegildo. *Automatic Exploitation of Non-Determinate Independent And-Parallelism in the Basic Andorra Model*. Num. FIM106.1/AI/91, Facultad de Informatica, U. P. Madrid, Spain, October 1991.
142. F. Bueno, M. Hermenegildo. *Results on Automatic Translation from Prolog to the Andorra Kernel Language*. Num. CLIP2/91.0, T.U. of Madrid (UPM), September 1991. theory in technical report, "Towards an Automatic Translation Algorithm from Prolog to the Andorra Kernel Language".
143. G. Gupta, M. Hermenegildo. *ACE: And/Or-parallel Copying-based Execution of Logic Programs*. Num. without, T.U. of Madrid (UPM), June 1991. also in Proc. ICLP91 Workshop on Parallel Execution of Logic Programs.

144. F. Bueno, M. Hermenegildo. *An Automatic Translation Scheme from Prolog to the Andorra Kernel Language*. Num. CLIP1/91.0, T.U. of Madrid, June 1991. also(first version) in Proc. GULP91, results and benchmarks in "Results on Automatic Translation from Prolog to the Andorra Kernel Language".
145. M. Carro, L. Gómez, M. Hermenegildo. *VISANDOR: A Tool for Visualizing And-/Or-parallelism in Logic Programs*. Num. CLIP9/91.0, 17 pages, T.U. of Madrid (UPM), June 1991.
146. G. Gupta, V. Santos-Costa, R. Yang, M. Hermenegildo. *IDIOM: A Model Intergrating Dependent-, Independent-, and Or-parallelism*. Num. CLIP8/91.0, University of Bristol, March 1991.
147. F. Giannotti, M. Hermenegildo. *A Technique for Recursive Invariance Detection and Selective Program Specialization*. Num. CLIP7/91.0, U. of Madrid (UPM), February 1991.
148. M. García de la Banda, M. Hermenegildo. *Analyzing Constraint Logic Programs*. Num. CLIP4/91.0, U. of Madrid (UPM), January 1991. Presented in ICLP-91 Workshop on Constraint Logic Programming, Paris.
149. K. Muthukumar, M. Hermenegildo. *Combined Determination of Sharing and Freeness of Program Variables Through Abstract Interpretation*. Num. STP-368-90, Microelectronics and Computer Technology Corporation (MCC), Austin, TX 78759, December 1990.
150. K. Shen, M. Hermenegildo. *A Simulation Study of Or- and Independent And-parallelism*. Num. FIM-60.1/AI/90, Facultad de Informatica, U. P. Madrid, 28660 Boadilla del Monte, Madrid, Spain, October 1990.
151. K. Muthukumar, M. Hermenegildo. *Deriving A Fixpoint Computation Algorithm for Top-down Abstract Interpretation of Logic Programs*. Num. ACT-DC-153-90, Microelectronics and Computer Technology Corporation (MCC), Austin, TX 78759, April 1990.
152. C. Carreras, M. Hermenegildo. *Towards an Analytical Model of the Data Diffusion Machine*. UPM (PEPMA ESPRIT Project technical report), 1990.
153. M. Hermenegildo, R. I. Nasr. *A Tool for Visualizing Independent And-parallelism in Logic Programs*. Num. CLIP1/90.0, T.U. of Madrid (UPM), 1990. Presented at the NACLIP-90 Workshop on Parallel Logic Programming, Austin, TX.
154. M. Hermenegildo, K. Greene. *&-Prolog and its Performance: Exploiting Independent And-Parallelism*. Num. ACA-ST-536-89, MCC, Austin, TX 78759, November 1989.
155. M. Hermenegildo, F. Rossi. *Strict and Non-Strict Independent And-Parallelism in Logic Programs: Correctness, Efficiency, and Compile-Time Conditions*. Num. ACA-ST-537-89, Microelectronics and Computer Technology Corporation (MCC), Austin, TX 78759, November 1989.
156. M. Hermenegildo, K. Muthukumar, K. Greene, F. Rossi, R. I. Nasr. *An Overview of the PAL Project*. Num. ACT-ST-234-89, Microelectronics and Computer Technology Corporation (MCC), Austin, TX 78759, September 1989.
157. K. Muthukumar, M. Hermenegildo. *Determination of Variable Dependence Information at Compile-Time Through Abstract Interpretation*. Num. ACA-ST-232-89, Microelectronics and Computer Technology Corporation (MCC), Austin, TX 78759, March 1989.
158. K. Muthukumar, M. Hermenegildo. *Methods for Automatic Compile-time Parallelization of Logic Programs using Independent/Restricted And-parallelism*. Num. ACA-ST-233-89, Microelectronics and Computer Technology Corporation (MCC), Austin, TX 78759, March 1989.
159. M. Hermenegildo, F. Rossi. *On the Correctness and Efficiency of Independent And-Parallelism in Logic Programs*. Num. ACA-ST-032-89, Microelectronics and Computer Technology Corporation (MCC), Austin, TX 78759, January 1989.

160. K. Muthukumar, M. Hermenegildo. *Efficient Methods for Supporting Side Effects in Independent And-parallelism and Their Backtracking Semantics*. Num. ACA-ST-031-89, Microelectronics and Computer Technology Corporation (MCC), Austin, TX 78759, January 1989.
161. R. Warren, M. Hermenegildo, S.K. Debray. *MA3: A System for Automatic Generation of CGEs*. Microelectronics and Computer Technology Corporation (MCC), Austin, TX 78759, February 1988.
162. M. Hermenegildo, E. Tick. *Memory Performance of AND-Parallel Prolog on Shared-Memory Architectures*. Num. ACA-ST-036-88, Microelectronics and Computer Technology Corporation (MCC), Austin, TX 78759, January 1988.
163. R. Warren, M. Hermenegildo. *On the Practicality of Global Flow Analysis of Logic Programs*. Num. ACA-126-88, Microelectronics and Computer Technology Corporation (MCC), Austin, TX 78759, January 1988.
164. M. Hermenegildo, E. Tick. *Performance Evaluation of the RAP-WAM Restricted AND-Parallel Architecture on Shared Memory Multiprocessors*. Num. PP-085-87, Microelectronics and Computer Technology Corporation (MCC), Austin, TX 78759, March 1987.
165. R. Warren, M. Hermenegildo. *Experimenting with Prolog: An Overview*. Num. ACA/PP 43, Microelectronics and Computer Technology Corporation (MCC), 3500 W. Balcones Center Dr. Austin, TX 78759, March 1987.
166. M. Hermenegildo, P. McGehearty. *Address Escaping and Reference Classification in the Design of a Cached, Multiple Cluster, Shared-Memory Architecture*. Num. PP-SRS-TM-12, MCC, Parallel Processing Program, 1987.
167. M. Hermenegildo. *Relating Goal Scheduling, Precedence, and Memory Management in AND-Parallel Execution of Logic Programs*. Num. PP-408-86, Microelectronics and Computer Technology Corporation (MCC), Austin, TX 78759, December 1986.
168. M. Hermenegildo, G. J. Lipovski, R. Warren. *Goal Scheduling and Memory Management in Parallel Logic Systems*. Num. PP-083-86, Microelectronics and Computer Technology Corporation (MCC), Austin, TX 78759, March 1986.
169. M. Hermenegildo. *A Restricted AND-parallel Execution Model and Abstract Machine for Prolog Programs*. Num. PP-104-85, Microelectronics and Computer Technology Corporation (MCC), Austin, TX 78759, October 1985.